

4

3

DWG. NO. 2708-XS SH REV. A

1

D

D

C

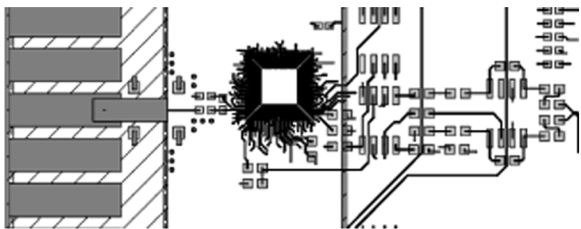
C

B

B

A

A



- 1.Top_Signal_1
- 2.Power - Local Ground
- 1.Signal_2
- 3.Power
- 4.Power
- 1.Signal_3
- 5.Ground
- 6.Bottom - Local Ground, Signal_4



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .xxx DO NOT SCALE DRAWING			CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT			APPROVALS	DATE	TITLE PSEC2 Flip Chip ADC Module PCB Xsection			
FINISH			DRAWN M. Bogdan	8/19/2010	SIZE	FSCM NO.	DWG. NO.	REV.
SIMILAR TO			CHECKED M. Bogdan	8/19/2010			2708 - XS	A
ACT. WT	CALC. WT	ISSUED			SCALE	1/2	SHEET	

4

3

2

1