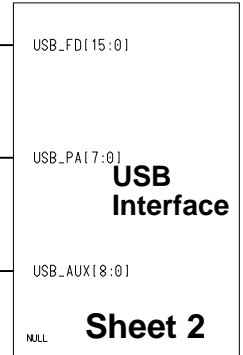
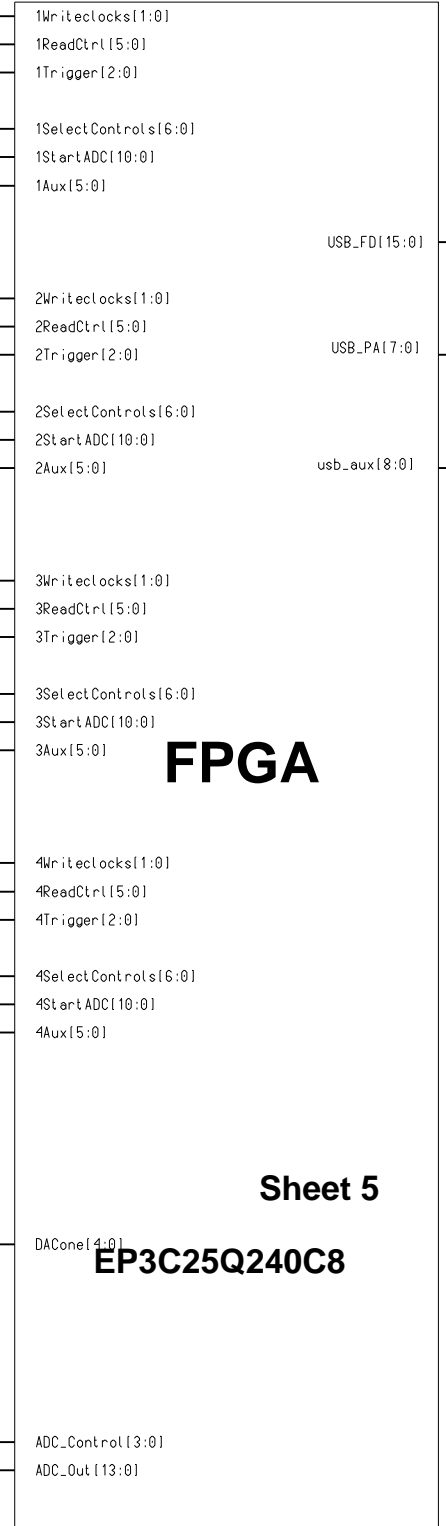
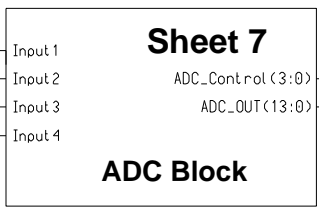
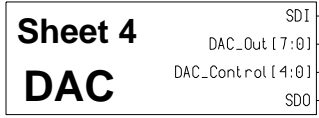
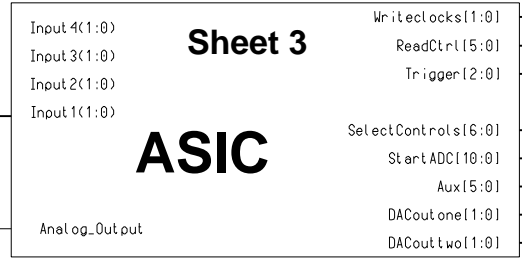
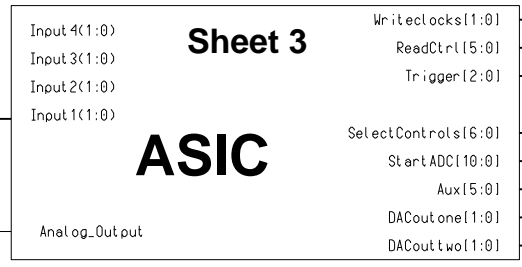
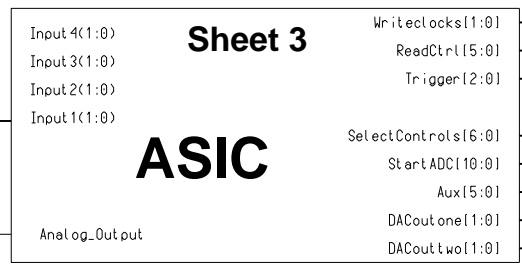
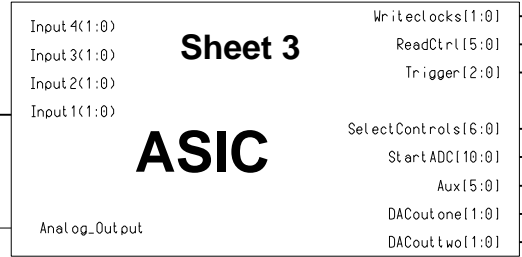


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 Strip38(1:0)
 Strip39(1:0)
 Strip40(1:0)

Strip Connection Sheet 6



Engineer: **M. Bogdan**
 Drawn by: **M. Bogdan**
 DATE: **8/16/2010**

The University of Chicago
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PSEC2 Flip Chip ADC Module
 Top Level

REV. A DRW. **2707** Sheet 1 of 7