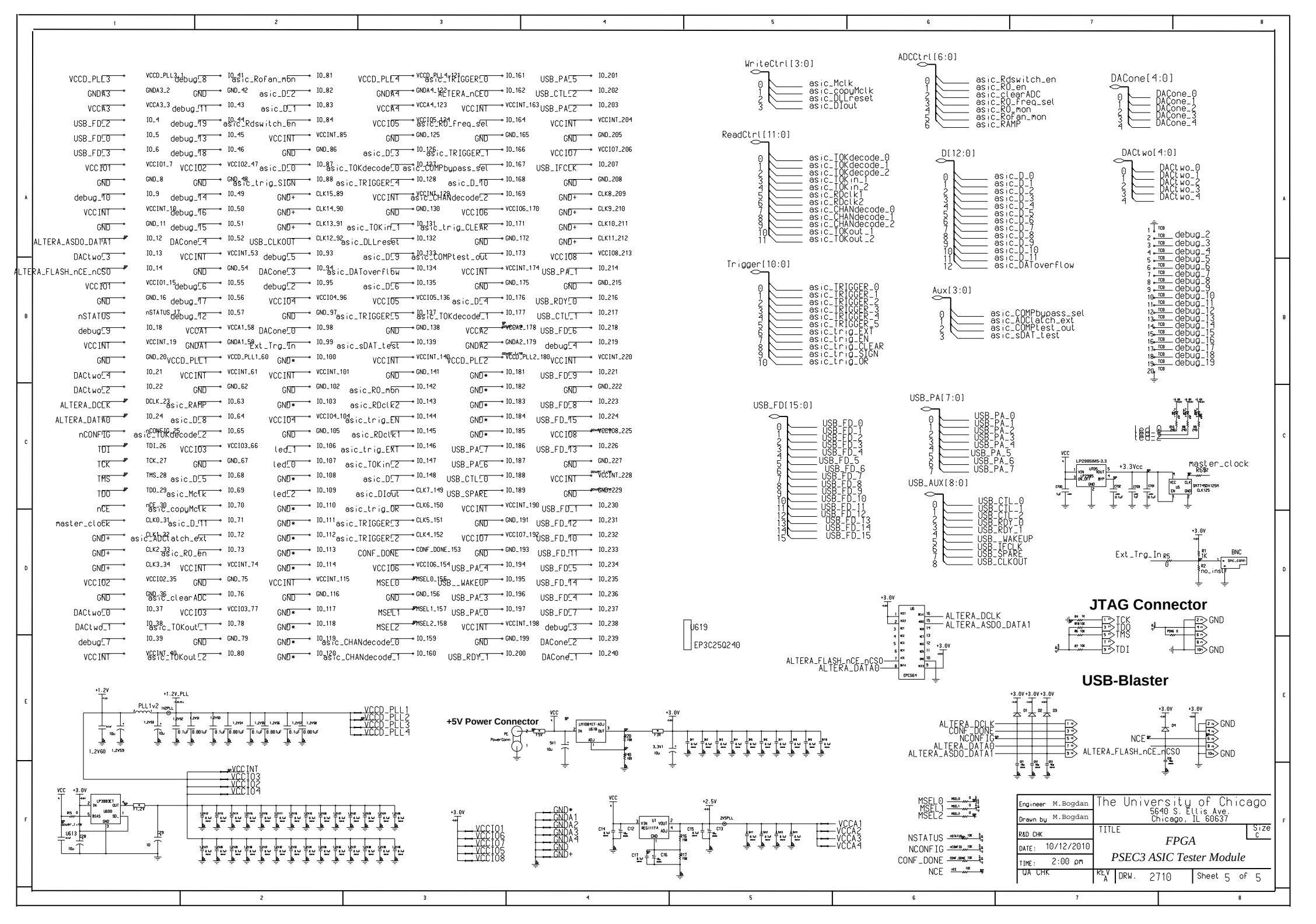
WriteCtrl[3:0]
asic_Mclk
asic_copyMclk
asic_DLLreset
asic_DIout
ADCCtrl[6:0]
asic_Rdswitch_en
asic_RO_en
asic_clearADC
asic_RO_freq_sel
asic_RO_mon
asic_Rofan_mon
asic_RAMP
DACone[4:0]
DACone_0
DACone_1
DACone_2
DACone_3
DACone_4
ReadCtrl[11:0]
asic_TOKdecode_0
asic_TOKdecode_1
asic_TOKdecode_2
asic_TOKin_1
asic_TOKin_2
asic_RDclk1
asic_RDclk2
asic_CHANdecode_0
asic_CHANdecode_1
asic_CHANdecode_2
asic_TOKout_1
asic_TOKout_2
D[12:0]
asic_D_0
asic_D_1
asic_D_2
asic_D_3
asic_D_4
asic_D_5
asic_D_6
asic_D_7
asic_D_8
asic_D_9
asic_D_10
asic_D_11
asic_DATOverflow
DACtwo[4:0]
DACtwo_0
DACtwo_1
DACtwo_2
DACtwo_3
DACtwo_4
Trigger[10:0]
asic_TRIGGER_0
asic_TRIGGER_1
asic_TRIGGER_2
asic_TRIGGER_3
asic_TRIGGER_4
asic_TRIGGER_5
asic_trig_EXT
asic_trig_EN
asic_trig_CLEAR
asic_trig_SIGN
asic_trig_OR
Aux[3:0]
asic_COMPbypass_sel
asic_ADCclatch_ext
asic_COMPtest_out
asic_sDAT_test
USB_FD[15:0]
USB_FD_0
USB_FD_1
USB_FD_2
USB_FD_3
USB_FD_4
USB_FD_5
USB_FD_6
USB_FD_7
USB_FD_8
USB_FD_9
USB_FD_10
USB_FD_11
USB_FD_12
USB_FD_13
USB_FD_14
USB_FD_15
USB_PA[7:0]
USB_PA_0
USB_PA_1
USB_PA_2
USB_PA_3
USB_PA_4
USB_PA_5
USB_PA_6
USB_PA_7
USB_AUX[8:0]
USB_CTL_0
USB_CTL_1
USB_RDY_0
USB_RDY_1
USB_WAKEUP
USB_IFCLK
USB_SPARE
USB_CLKOUT
U619
EP3C25Q240
ALTERA_DCLK
ALTERA_ASDO_DATA1
ALTERA_FLASH_nCE_nCSO
ALTERA_DATA0
JTAG Connector
TCK
TDO
TMS
TDI
GND
USB-Blaster
+5V Power Connector
VCCD_PLL1
VCCD_PLL2
VCCD_PLL3
VCCD_PLL4
VCCINT
VCCIO3
VCCIO2
VCCIO4
VCCIO1
VCCIO6
VCCIO7
VCCIO5
VCCIO8
GND*
GND1
GND2
GND3
GND4
GND
GND+
VCCA1
VCCA2
VCCA3
VCCA4
MSEL0
MSEL1
MSEL2
NSTATUS
NCONFIG
CONF_DONE
NCE
Engineer M.Bogdan
Drawn by M.Bogdan
R&D CHK
DATE: 10/12/2010
TIME: 2:00 pm
QA CHK
The University of Chicago
5640 S. Ellis Ave.
Chicago, IL 60637
TITLE
FPGA
PSEC3 ASIC Tester Module
Size C
REV A
DRW. 2710
Sheet 5 of 5