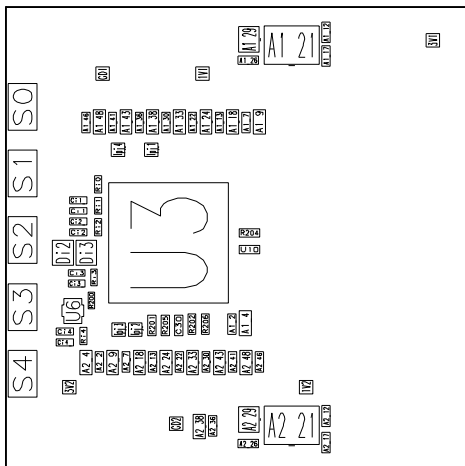


4

3

2

1



Notes:

- 1. Some parts will not be installed, see BOM file.
- 3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES -XX DO NOT SCALE DRAWING	CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
	APPROVALS	DATE	TITLE		
TREATMENT	DRAWN M. Bogdan	2/17/2011	ASIC Tester Board Assembly drawing - Top		
FINISH	CHECKED M. Bogdan	2/17/2011	SIZE C	FIG. NO.	REV. A
SIMILAR TO	ACT. WT	CALC. WT	ISSUED	2727 Top	
			SCALE		SHEET 1 of 1

THIS SHEET IS COMPUTER GENERATED

4

3

2

1

D  
C  
B  
A  
REV.  
SH  
Dwg. No.