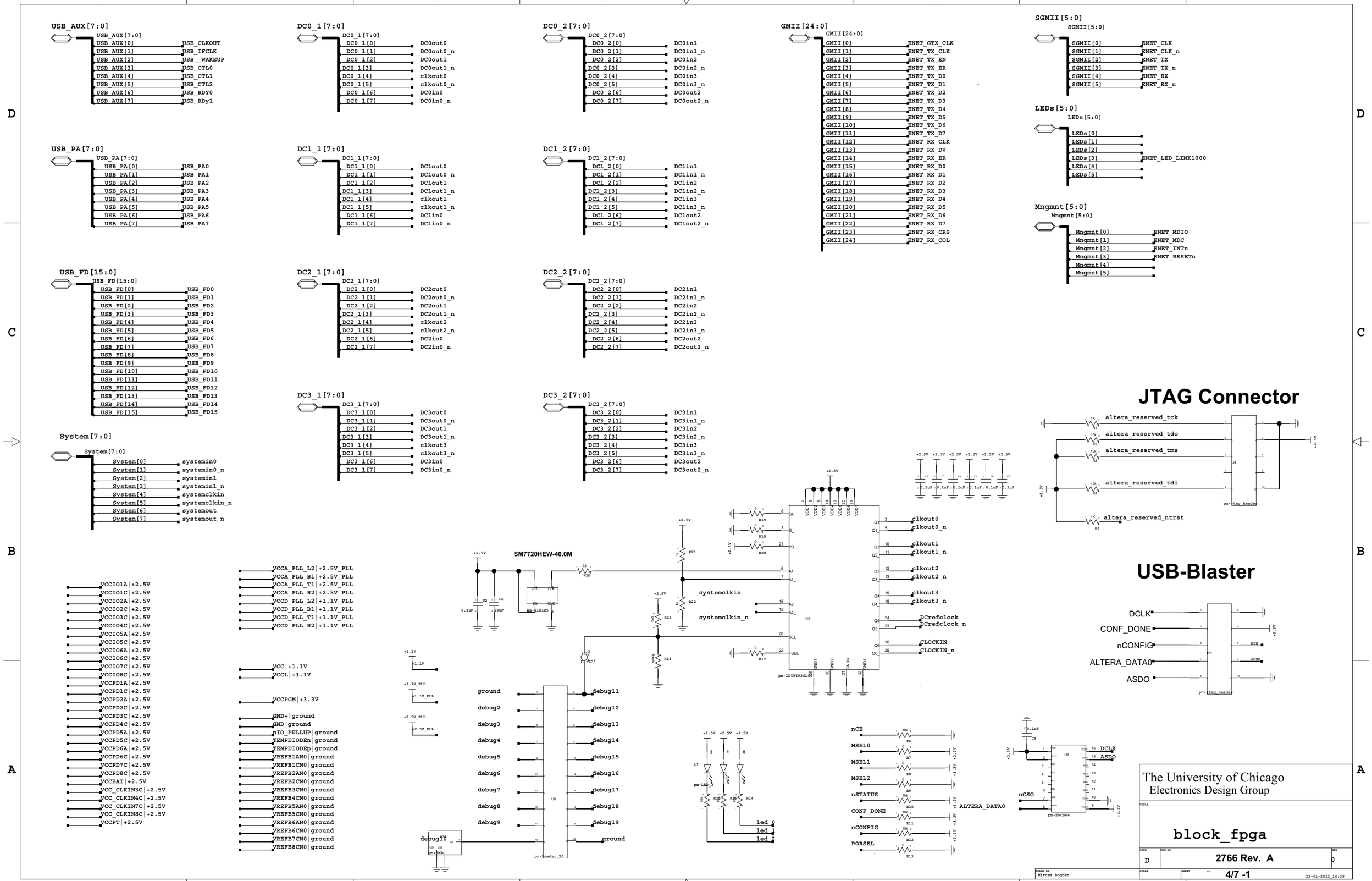
USB_AUX[7:0]
USB_AUX[0] USB_CLKOUT
USB_AUX[1] USB_IFCLK
USB_AUX[2] USB_WAKEUP
USB_AUX[3] USB_CTL0
USB_AUX[4] USB_CTL1
USB_AUX[5] USB_CTL2
USB_AUX[6] USB_RDY0
USB_AUX[7] USB_RDy1
USB_PA[7:0]
USB_PA[0] USB_PA0
USB_PA[1] USB_PA1
USB_PA[2] USB_PA2
USB_PA[3] USB_PA3
USB_PA[4] USB_PA4
USB_PA[5] USB_PA5
USB_PA[6] USB_PA6
USB_PA[7] USB_PA7
USB_FD[15:0]
USB_FD[0] USB_FD0
USB_FD[1] USB_FD1
USB_FD[2] USB_FD2
USB_FD[3] USB_FD3
USB_FD[4] USB_FD4
USB_FD[5] USB_FD5
USB_FD[6] USB_FD6
USB_FD[7] USB_FD7
USB_FD[8] USB_FD8
USB_FD[9] USB_FD9
USB_FD[10] USB_FD10
USB_FD[11] USB_FD11
USB_FD[12] USB_FD12
USB_FD[13] USB_FD13
USB_FD[14] USB_FD14
USB_FD[15] USB_FD15
System[7:0]
System[0] systemin0
System[1] systemin0_n
System[2] systemin1
System[3] systemin1_n
System[4] systemclkin
System[5] systemclkin_n
System[6] systemout
System[7] systemout_n
DC0_1[7:0]
DC0_1[0] DC0out0
DC0_1[1] DC0out0_n
DC0_1[2] DC0out1
DC0_1[3] DC0out1_n
DC0_1[4] clkout0
DC0_1[5] clkout0_n
DC0_1[6] DC0in0
DC0_1[7] DC0in0_n
DC1_1[7:0]
DC1_1[0] DC1out0
DC1_1[1] DC1out0_n
DC1_1[2] DC1out1
DC1_1[3] DC1out1_n
DC1_1[4] clkout1
DC1_1[5] clkout1_n
DC1_1[6] DC1in0
DC1_1[7] DC1in0_n
DC2_1[7:0]
DC2_1[0] DC2out0
DC2_1[1] DC2out0_n
DC2_1[2] DC2out1
DC2_1[3] DC2out1_n
DC2_1[4] clkout2
DC2_1[5] clkout2_n
DC2_1[6] DC2in0
DC2_1[7] DC2in0_n
DC3_1[7:0]
DC3_1[0] DC3out0
DC3_1[1] DC3out0_n
DC3_1[2] DC3out1
DC3_1[3] DC3out1_n
DC3_1[4] clkout3
DC3_1[5] clkout3_n
DC3_1[6] DC3in0
DC3_1[7] DC3in0_n
DC0_2[7:0]
DC0_2[0] DC0in1
DC0_2[1] DC0in1_n
DC0_2[2] DC0in2
DC0_2[3] DC0in2_n
DC0_2[4] DC0in3
DC0_2[5] DC0in3_n
DC0_2[6] DC0out2
DC0_2[7] DC0out2_n
DC1_2[7:0]
DC1_2[0] DC1in1
DC1_2[1] DC1in1_n
DC1_2[2] DC1in2
DC1_2[3] DC1in2_n
DC1_2[4] DC1in3
DC1_2[5] DC1in3_n
DC1_2[6] DC1out2
DC1_2[7] DC1out2_n
DC2_2[7:0]
DC2_2[0] DC2in1
DC2_2[1] DC2in1_n
DC2_2[2] DC2in2
DC2_2[3] DC2in2_n
DC2_2[4] DC2in3
DC2_2[5] DC2in3_n
DC2_2[6] DC2out2
DC2_2[7] DC2out2_n
DC3_2[7:0]
DC3_2[0] DC3in1
DC3_2[1] DC3in1_n
DC3_2[2] DC3in2
DC3_2[3] DC3in2_n
DC3_2[4] DC3in3
DC3_2[5] DC3in3_n
DC3_2[6] DC3out2
DC3_2[7] DC3out2_n
GMII[24:0]
GMII[0] ENET_GTX_CLK
GMII[1] ENET_TX_CLK
GMII[2] ENET_TX_EN
GMII[3] ENET_TX_ER
GMII[4] ENET_TX_D0
GMII[5] ENET_TX_D1
GMII[6] ENET_TX_D2
GMII[7] ENET_TX_D3
GMII[8] ENET_TX_D4
GMII[9] ENET_TX_D5
GMII[10] ENET_TX_D6
GMII[11] ENET_TX_D7
GMII[12] ENET_RX_CLK
GMII[13] ENET_RX_DV
GMII[14] ENET_RX_ER
GMII[15] ENET_RX_D0
GMII[16] ENET_RX_D1
GMII[17] ENET_RX_D2
GMII[18] ENET_RX_D3
GMII[19] ENET_RX_D4
GMII[20] ENET_RX_D5
GMII[21] ENET_RX_D6
GMII[22] ENET_RX_D7
GMII[23] ENET_RX_CRS
GMII[24] ENET_RX_COL
SGMII[5:0]
SGMII[0] ENET_CLK
SGMII[1] ENET_CLK_n
SGMII[2] ENET_TX
SGMII[3] ENET_TX_n
SGMII[4] ENET_RX
SGMII[5] ENET_RX_n
LEDs[5:0]
LEDs[0]
LEDs[1]
LEDs[2]
LEDs[3] ENET_LED_LINK1000
LEDs[4]
LEDs[5]
Mngmnt[5:0]
Mngmnt[0] ENET_MDIO
Mngmnt[1] ENET_MDC
Mngmnt[2] ENET_INTn
Mngmnt[3] ENET_RESETn
Mngmnt[4]
Mngmnt[5]
JTAG Connector
altera_reserved_tck
altera_reserved_tdo
altera_reserved_tms
altera_reserved_tdi
altera_reserved_ntrst
pn-jtag_header
USB-Blaster
DCLK
CONF_DONE
nCONFIG
ALTERA_DATA0
ASDO
pn-jtag_header
VCCI01A|+2.5V
VCCI01C|+2.5V
VCCI02A|+2.5V
VCCI02C|+2.5V
VCCI03C|+2.5V
VCCI04C|+2.5V
VCCI05A|+2.5V
VCCI05C|+2.5V
VCCI06A|+2.5V
VCCI06C|+2.5V
VCCI07C|+2.5V
VCCI08C|+2.5V
VCCPD1A|+2.5V
VCCPD1C|+2.5V
VCCPD2A|+2.5V
VCCPD2C|+2.5V
VCCPD3C|+2.5V
VCCPD4C|+2.5V
VCCPD5A|+2.5V
VCCPD5C|+2.5V
VCCPD6A|+2.5V
VCCPD6C|+2.5V
VCCPD7C|+2.5V
VCCPD8C|+2.5V
VCCBAT|+2.5V
VCC_CLKIN3C|+2.5V
VCC_CLKIN4C|+2.5V
VCC_CLKIN7C|+2.5V
VCC_CLKIN8C|+2.5V
VCCPT|+2.5V
VCCA_PLL_L2|+2.5V_PLL
VCCA_PLL_B1|+2.5V_PLL
VCCA_PLL_T1|+2.5V_PLL
VCCA_PLL_R2|+2.5V_PLL
VCCD_PLL_L2|+1.1V_PLL
VCCD_PLL_B1|+1.1V_PLL
VCCD_PLL_T1|+1.1V_PLL
VCCD_PLL_R2|+1.1V_PLL
VCC|+1.1V
VCCL|+1.1V
VCCPGM|+3.3V
GND+|ground
GND|ground
nIO_PULLUP|ground
TEMPDIODEn|ground
TEMPDIODEp|ground
VREFB1AN0|ground
VREFB1CN0|ground
VREFB2AN0|ground
VREFB2CN0|ground
VREFB3CN0|ground
VREFB4CN0|ground
VREFB5AN0|ground
VREFB5CN0|ground
VREFB6AN0|ground
VREFB6CN0|ground
VREFB7CN0|ground
VREFB8CN0|ground
SM7720HEW-40.0M
systemclkin
systemclkin_n
pn-IDT5T93GL02
ground
debug2
debug3
debug4
debug5
debug6
debug7
debug8
debug9
debug10
debug11
debug12
debug13
debug14
debug15
debug16
debug17
debug18
debug19
ground
pn-header_20
led_0
led_1
led_2
nCE
MSEL0
MSEL1
MSEL2
nSTATUS
CONF_DONE
nCONFIG
PORSEL
ALTERA_DATA0
nCSO
pn-EPCS64
The University of Chicago
Electronics Design Group
block_fpga
2766 Rev. A
4/7 -1
Mircea Bogdan
23-01-2012_10:35