Multiple Digital Delay Units  
SERIES: MDU-38F  
(8 pins DIP) TTL Interfaced

Features:
- Auto-insertable.
- Completely interfaced for TTL & DTL application.
- P.C. board space economy achieved.
- Fits standard 8 pins DIP socket.
- 3 equal delay units/package.

Specifications:
- Delay Tolerance: ± 5% or 2 NS whichever is greater.
- Rise-time: 2 NS typically.
- Temperature coefficient: 100 PPM/° C.
- Temperature range: 0° to +70° C.
- Input signal requirement: TTL logic.
- Input pulse width: Min. 100% of total delay.
- Input rep. rate = min. 3 X pulse width.
- Output fan-out: TTL schottky load.
- Supply voltage: 5 Vdc ± 5%.
- Supply Current:
  \[ \text{Iccu: 74 ma.} \]
  \[ \text{Icch: 14 ma.} \]

Test Conditions:
- Input Pulse Width: > 150% of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time.
- All measurements made @ Vcc = 5V;
  \[ T_A = +25°C, \text{ no loads on taps.} \]

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Other delay times available on request.