Rows Z and D are in accordance with the VMEbus Extensions standard. Note: the positioning of rows A, B and C should still be in accordance with the VME Standard.

All user-defined signals on P2 are open-collector TTL excluding:

a. Geographical Addresses are pulled-up/grounded via 1KΩ
b. 122ns Clock is differential PECL: CLK = CLK*
c. ERROR* is open collector TTL