Bunch Counter Controls

Control(106) - Enable Read for L2BuF
Control(107) - Reset for Bunch Counter
Control(108) - VME Strobe for Bunch Counter
Control(67) - Read Clock/SD
Control(68) - Write Clock/SI
Control(69) - Reset
Control(70) - Empty Flag
Control(81) - Full Flag
Control(82) - Read Address B
Control(83) - Read Address A
Control(84) - Write Address B
Control(85) - Write Address A
Control(90) - Write Enable
Control(113) - FIFO REN
Control(115) - FIFO WEN

ISP INPUT
ISP.IN(0) - TDI From Previous Chip in Chain
ISP.IN(1) - TMS Signal
ISP.IN(2) - TOX Signal

ISP OUTPUT
ISP.OUT(0) - TDO To Next Chip in Chain
ISP.OUT(1) - TMS Signal (Tied to ISP.IN(1))
ISP.OUT(2) - TOX Signal (Tied to ISP.IN(2))