

# Digital Library for the 20 GS/s Sampling Chip

Schematics for the following generic digital components:

- inverter, and2, and3, and4;
- nand2, nand3, nand4nor2, nor3, nor4;
- dff, dff with clear;
- 2 and 4-bit hex-to-binary decoder;
- 12-bit counter;

Schematic for a comparator.

Note: components were drawn using nfet and pfet from cmrf8sf.