Sampling Chip – Test Bench
Status Report

The University of Chicago
Sampling Chip Test Card – QFP Version

Figure 1. The first Sampling Chip Test Card and the Stripline SMA Interface Card.
Sampling Chip Test Card – Flip Chip Rev.2

Specifications:

• Accommodate 4 pieces Sampling Chips;
• Interface with Detector Striplines and with SMA connectors;
• 4 Channels 12-BIT/50MHz ADC;
• Data Processing;
• USB Interface.
Sampling Flip Chip Test Card – ADC Block

8/2/2010 Mircea Bogdan
Figure 2. Partial Layout of the Sampling Flip Chip Test Card. Only one of the four channels is shown. Placement is preliminary.
Sampling Flip Chip Rev 2 Test Card - Status

Schematic capture is almost finished.

Layout started.

Plan to finish design by end of August.

Manufacture 4 pieces PCB and 2 pieces assembled cards.

Test Sampling Flip Chips Rev.2 in 2\textsuperscript{nd} half of September.