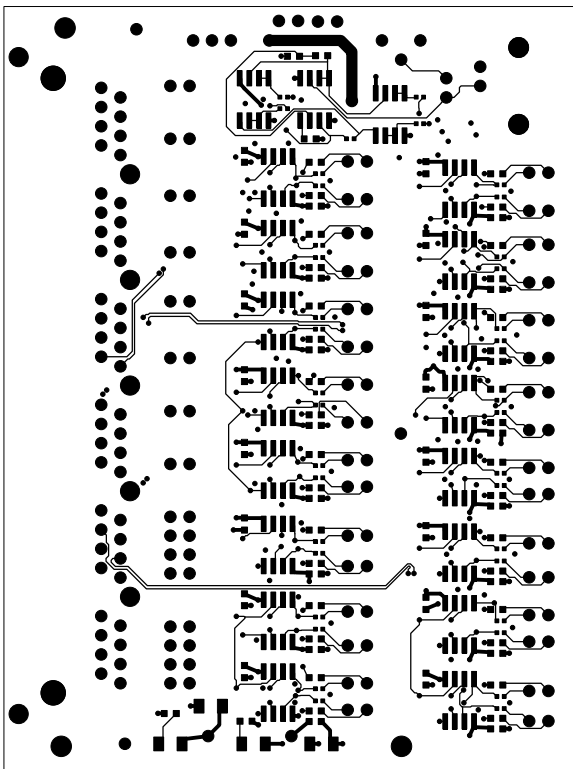


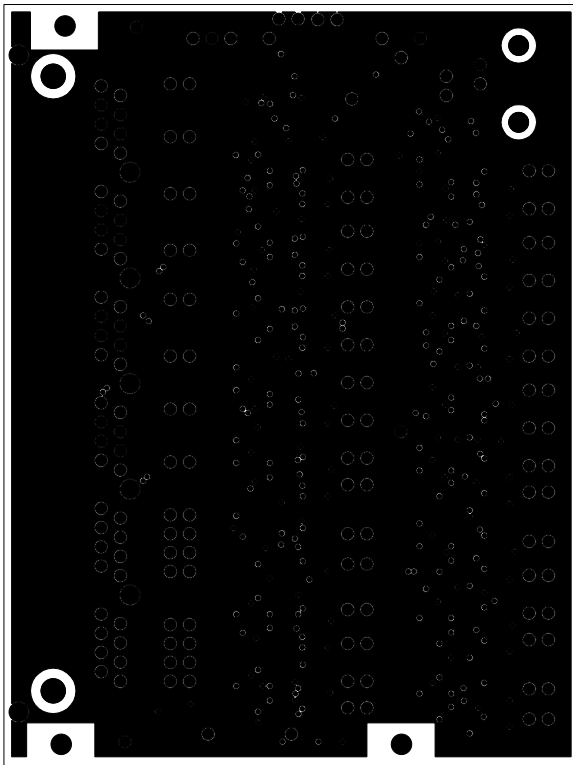
Through Holes						
Symbol	Diameter	Tolerance	Plated	Type	HoleName	Quantity
A	0.0140		Yes	Drill	Rnd 0.35560	302
B	0.0350		Yes	Drill	Rnd 0.889	50
C	0.0410		Yes	Drill	Rnd 1.0414	115
D	0.0620		Yes	Drill	Rnd 1.5748	7
E	0.1040		Yes	Drill	Rnd 2.6416	2
F	0.1280		Yes	Drill	Rnd 3.25	2
G	0.1060		No	Drill	Rnd 2.6924 Non-Plated	3

TOP: FILM1

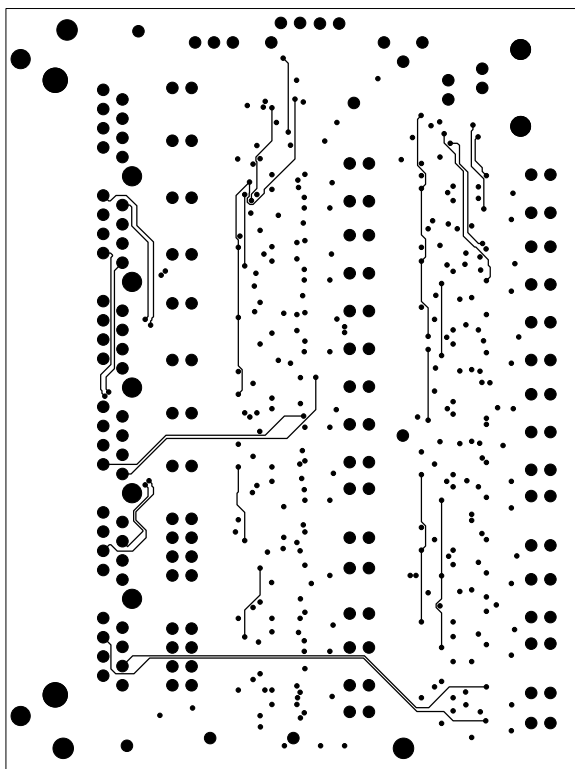
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Assy Drawing #3005-T  
3/12/2023  
16-Channel, 500MHz ADC Board Tester



POWER: GROUND, FILM2  
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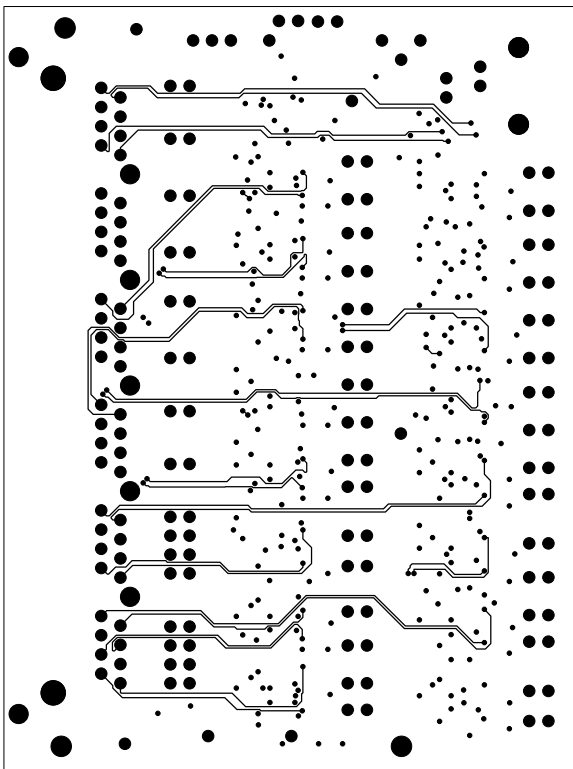


SIGNAL - FILM3  
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The University of Chicago  
Assy Drawing #3005-T  
3/12/2023  
16-Channel, 500MHz ADC Board Tester



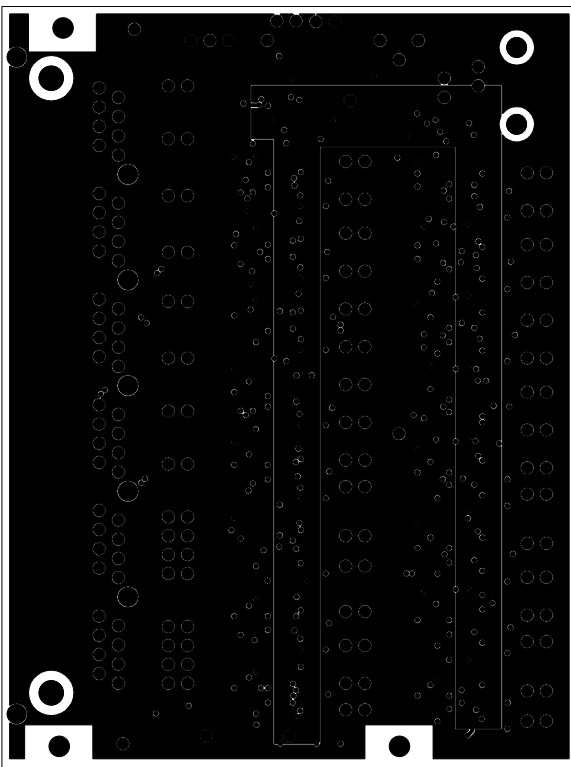
Mircea Bogdan  
The University of Chicago  
Assy Drawing #3005-T  
3/12/2023

SIGNAL - FILM4 16-Channel, 500MHz ADC Board Tester



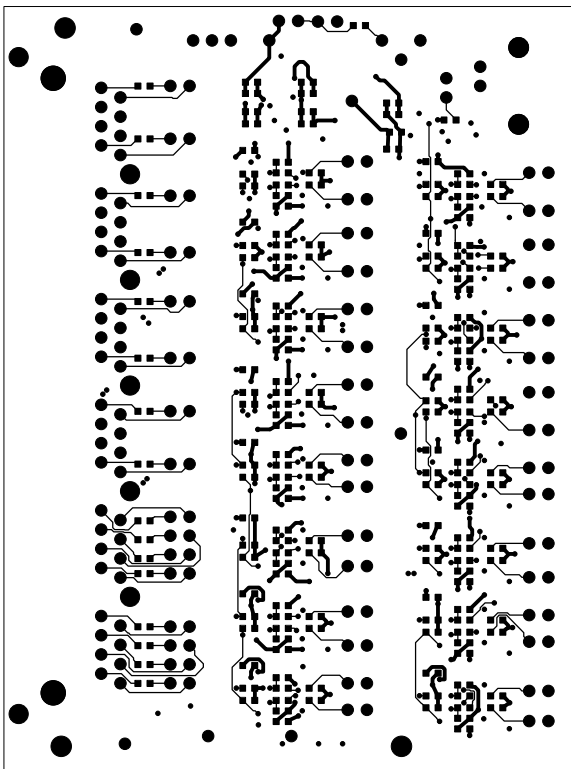
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POWER: FILM5



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Bottom -, FILM18



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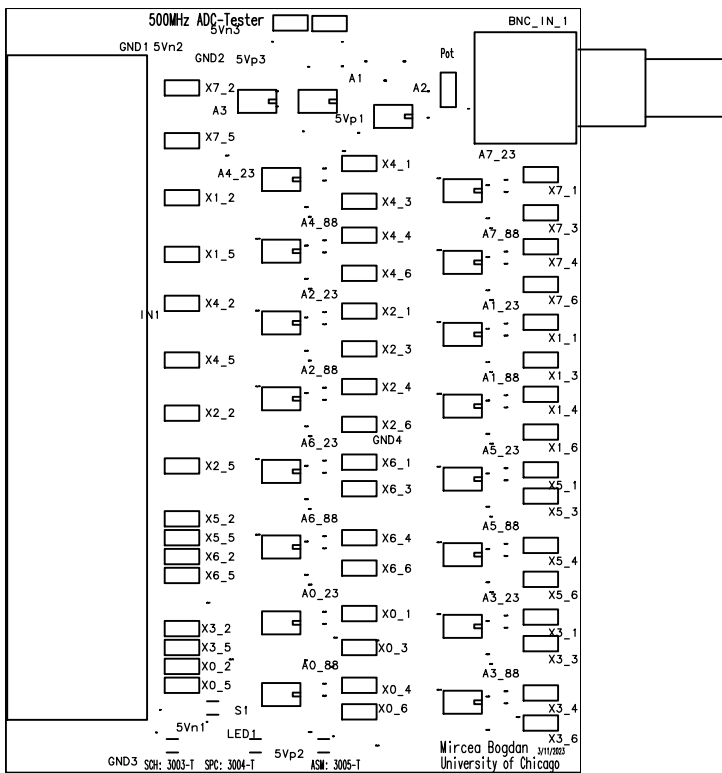
B0T SILK

218				
-	012	02		
	012	02		
	012	02	012	-
	012	02	012	02
-	T1_A	-	T1_A	-
	01_A	T1_A	01_A	T1_A
	02_A	02_A	02_A	02_A
-	001_A	-	001_A	-
	001_A	001_A	001_A	001_A
-	001_A	-	001_A	-
	001_A	001_A	001_A	001_A
-	001_A	-	001_A	-
	001_A	001_A	001_A	001_A
-	T1_SA	-	T1_SA	-
	01_SA	T1_SA	01_SA	T1_SA
	02_SA	02_SA	02_SA	02_SA
-	001_SA	-	001_SA	-
	001_SA	001_SA	001_SA	001_SA
-	001_SA	-	001_SA	-
	001_SA	001_SA	001_SA	001_SA
-	T1_BA	-	T1_BA	-
	01_BA	T1_BA	01_BA	T1_BA
	02_BA	02_BA	02_BA	02_BA
-	001_BA	-	001_BA	-
	001_BA	001_BA	001_BA	001_BA
-	001_BA	-	001_BA	-
	001_BA	001_BA	001_BA	001_BA
-	T1_OA	-	T1_OA	-
	01_OA	T1_OA	01_OA	T1_OA
	02_OA	02_OA	02_OA	02_OA
-	001_OA	-	001_OA	-
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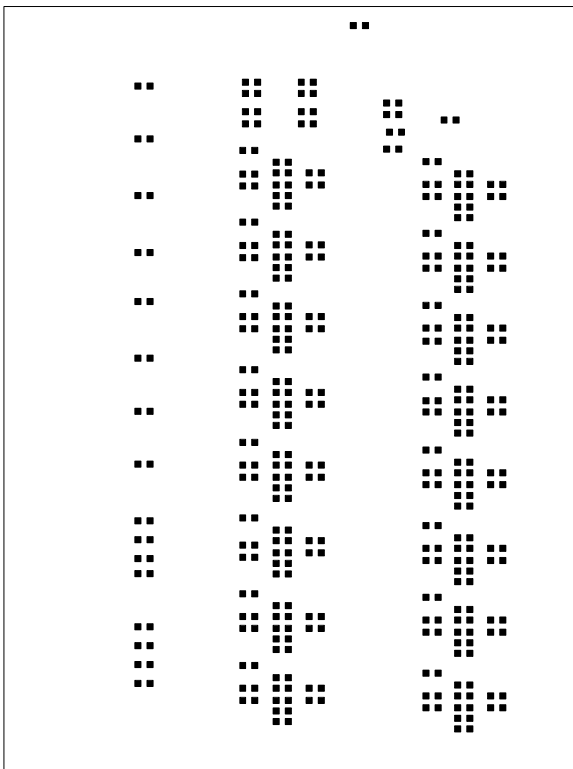
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TOP SILK



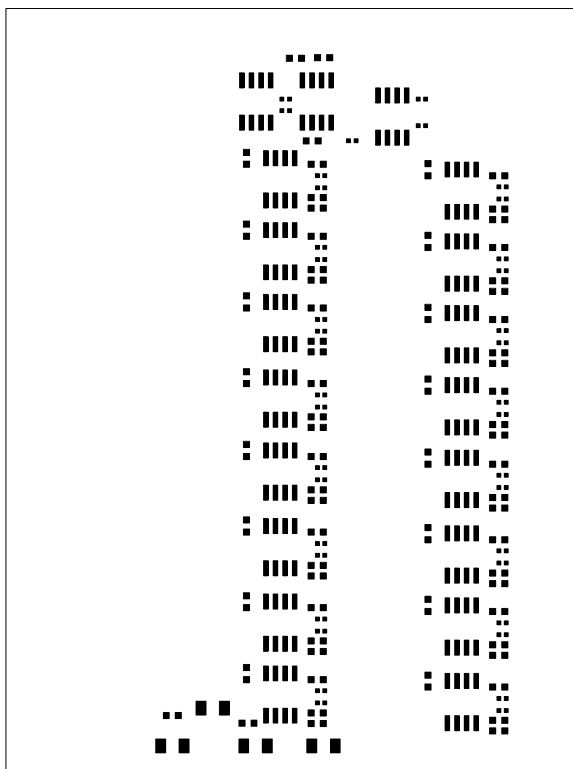
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BOT PASTE



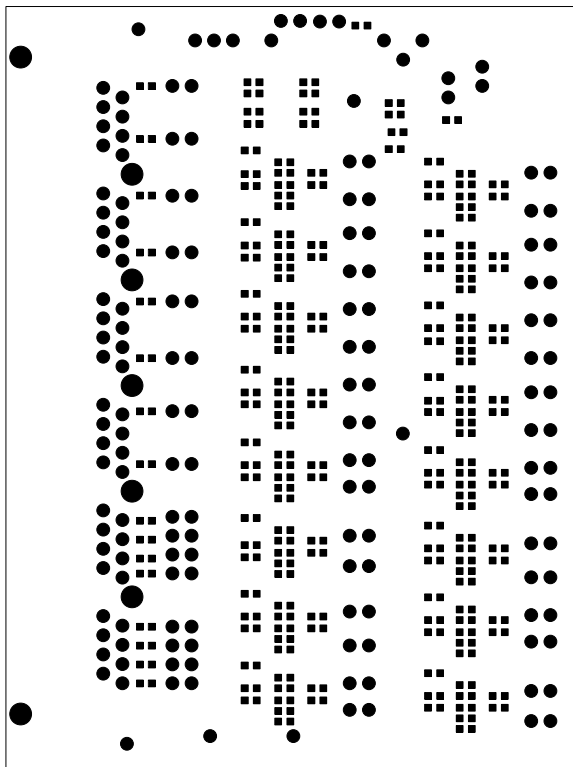
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TOP PASTE



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BOT MASK



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TOP MASK

