16 ADC Channels Power Requirements:

- 3.3V - 4A to make 2.5V
- 5V (Vcc) - 3A to make 1.8V, 3.3V

FPGA Max Power:
- 5V (Vcc) = 1.25A to make 1.1V, 1.15V
- +3.3V = 1A to make 2.5V, 1.5V

Clock Power:
- +5.5V = 0.7A max including the 2.5V
- +3.3V = 2A max

Power Estimate per Board

- Max Power from VME Slot

- 3.3V - 7.7A
- 5V - 4.25A
- +7V - 1.2A
- -7V - 0.4A

User Defined +/− 7V - 2 pins - 3A

The University of Chicago Power

500MHz 16Ch ADC Module