

4

3

2

1

D

D

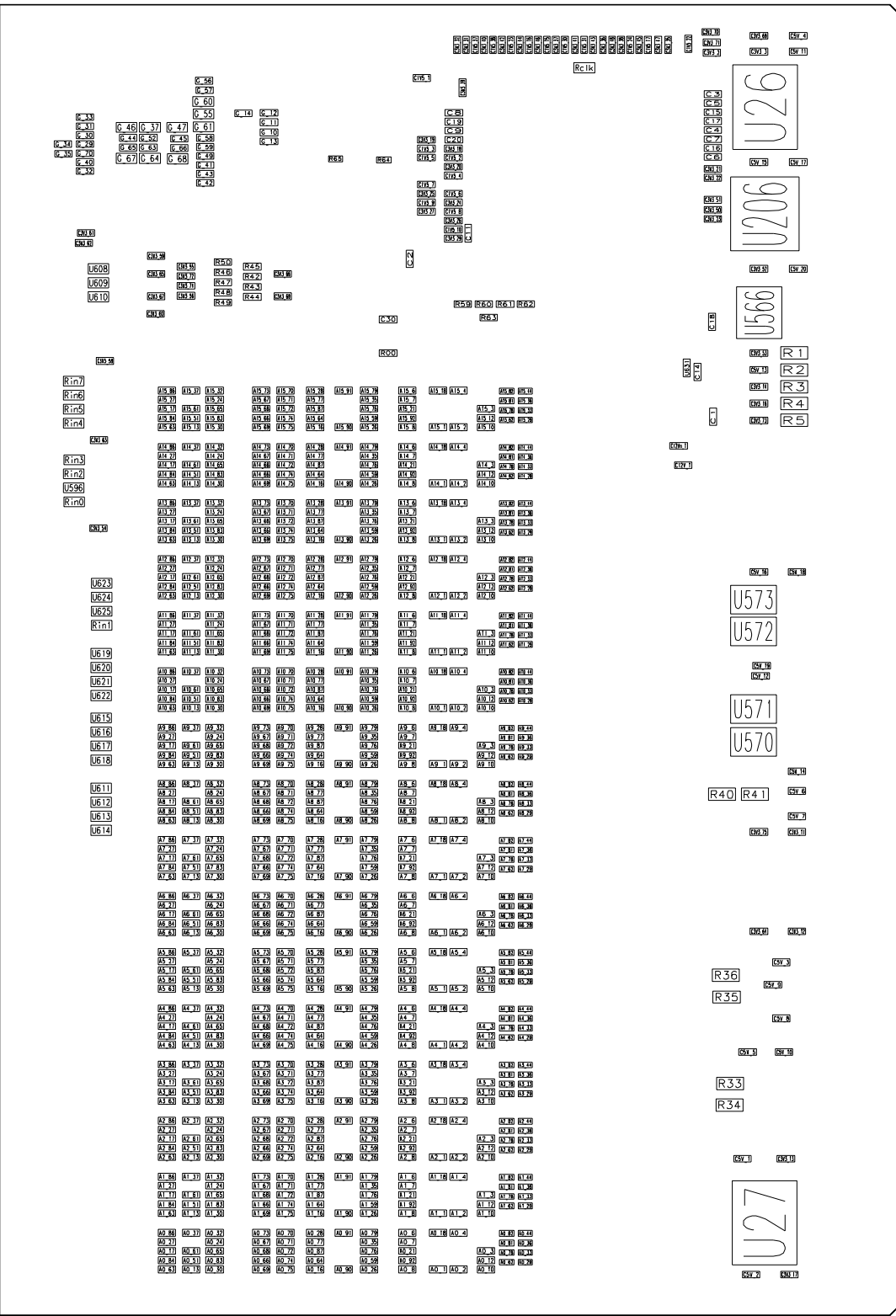
C

C

B

A

REV. SHEET NO.



Notes:

1. Some parts will not be installed, see BOM file.
2. No solder on or under P0.
3. No Centroid Data provided. Assemblt house shall generate it from Gerber Files if needed.
4. Component Clearance: 0.4 mm.

THIS SHEET IS COMPUTER GENERATED

4

3

2

1

A

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES .xxx DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT		APPROVALS	DATE	TITLE			
FINISH		DRAWN M. Bogdan	6/14/07	14-BIT-ADC Board Assembly drawing - Bottom			
SIMILAR TO		CHECKED M. Bogdan	6/14/07	SIZE C	ESCH. NO.	DWG. NO. B-2607-Bot	REV. A
ACT. WT	CALC. WT	ISSUED		SCALE	SHEET 1 of 1		