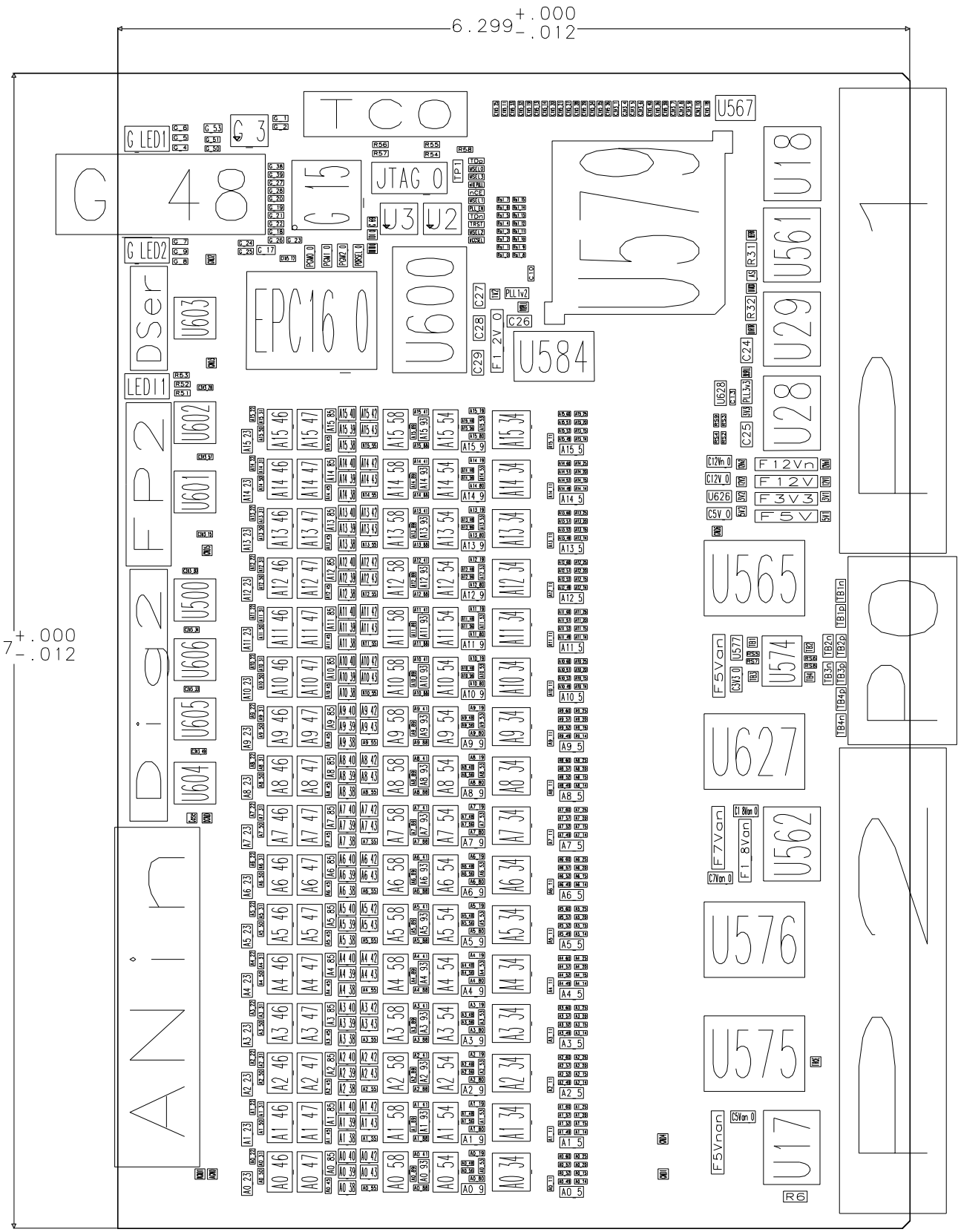

Notes:

1. Some parts will not be installed, see BOM file.
2. No solder on or under PO.
3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX .XXX DO NOT SCALE DRAWING	CONTRACT NO.	UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
	APPROVALS	DATE	TITLE 14-BIT-ADC Board Assembly drawing - Top
TREATMENT	DRAWN M.Bogdan	6/14/07	
FINISH	CHECKED M.Bogdan	6/14/07	SIZE C / FSCM NO. / DWG. NO. B-2607-Top / REV. A
SIMILAR TO / ACT. WT / CALC WT	ISSUED		SCALE / SHEET 1 of 1

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