Front Panel - digital input

JPARC-K 14BIT-ADC Board
All 33 Ohm resistors are 0402.

Notes:

- In = 0V → E1=0V → E2=+1V → 11 1111 1111 1111
- In = -0.5V → E1=-1V → E2=-1V → 00 0000 0000 0000

There are 16 channels on one board.

AD9254 receives LVDS clock from the FPGA.

AD9254 sends 14-BIT data bus and clock to FPGA.

All 0.1uF caps are Panasonic ECJ-1VB1C104K - 0603.

All 33 Ohm resistors are 0402; All other resistors are 0603.