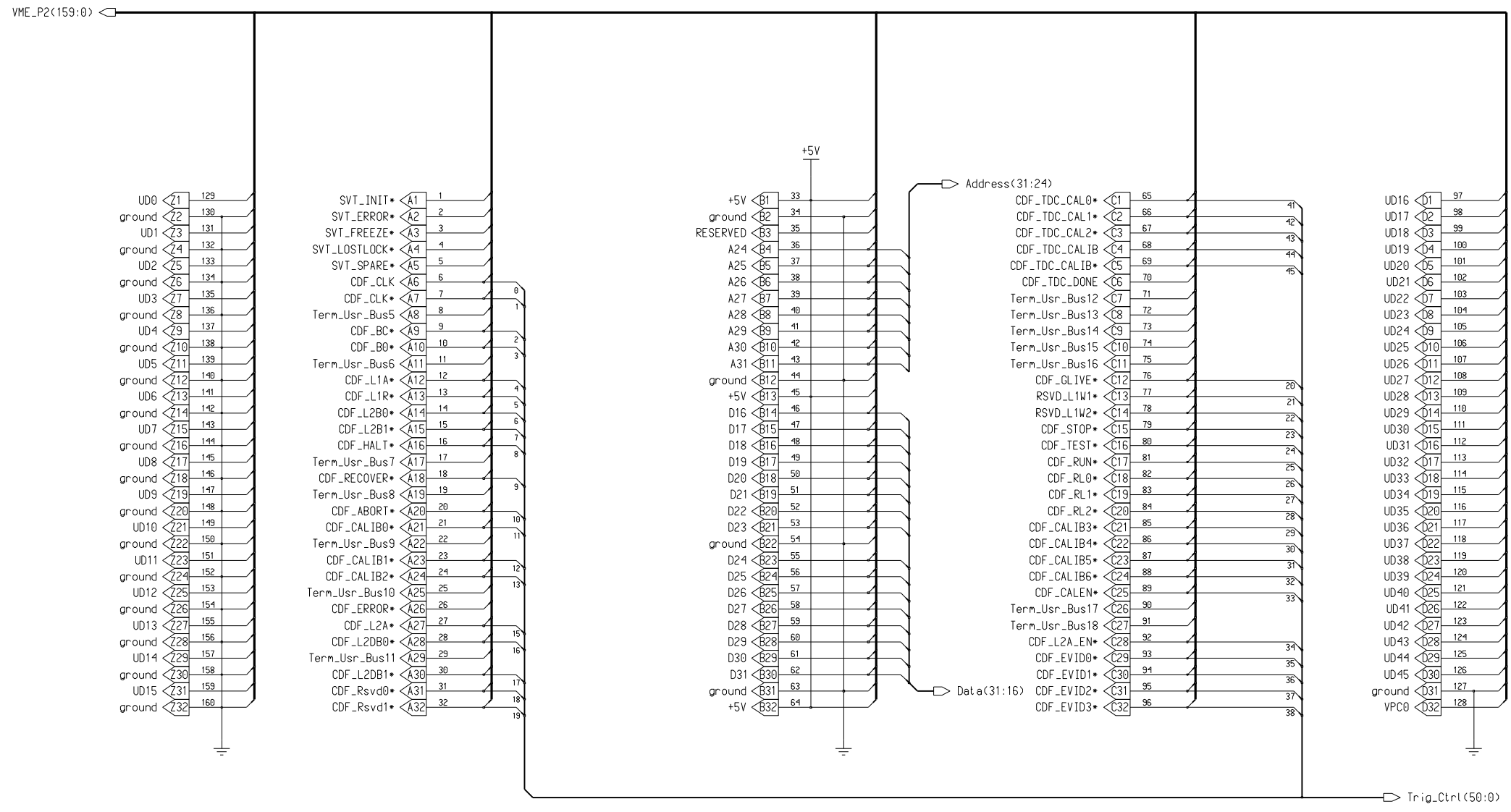


P2



Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M. Bogdan		
R&D CHK		TITLE	Size B
DATE:	3/1/05	VME-P2 14-BIT ADC Board	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-2605 Sheet 11 of 12