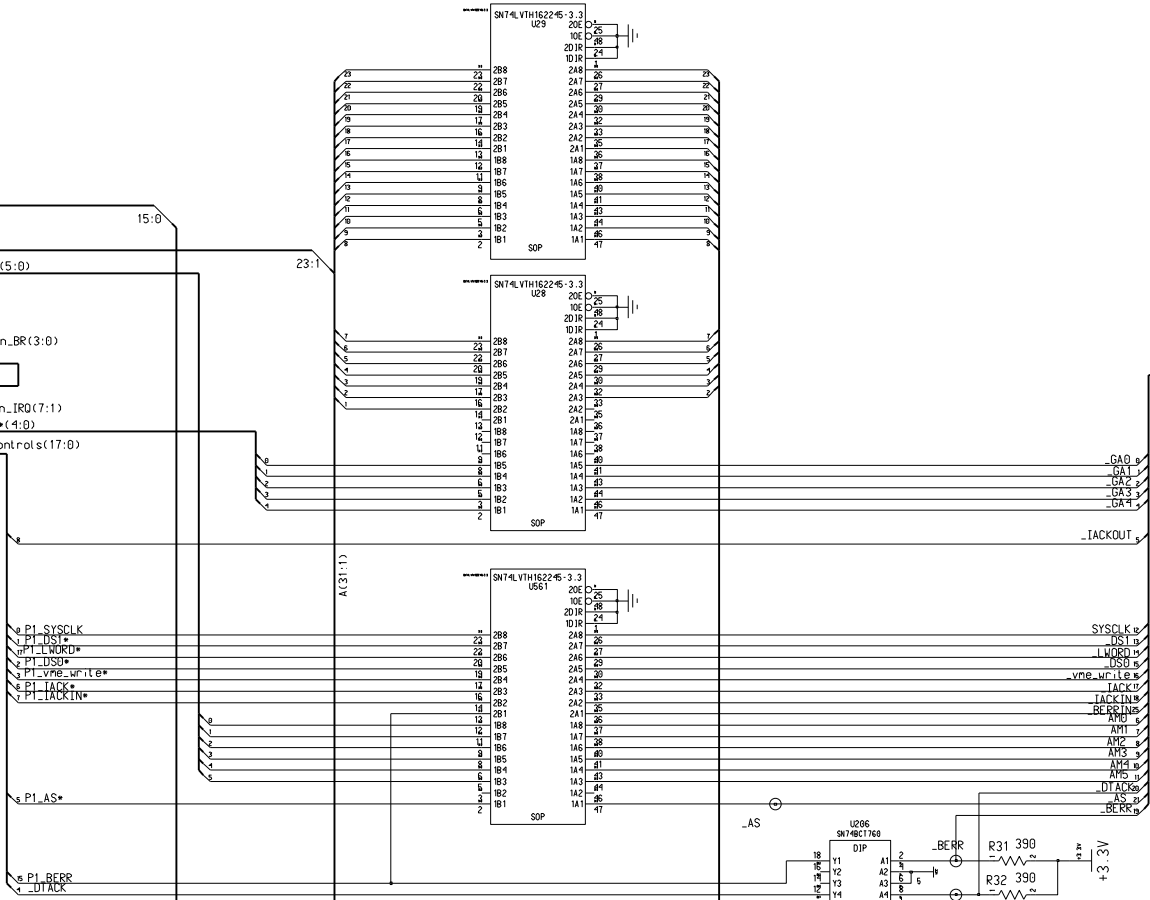
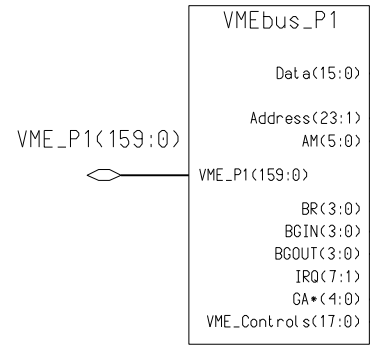
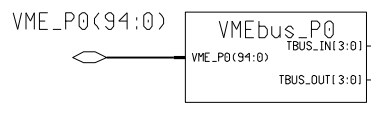


Sheet 9

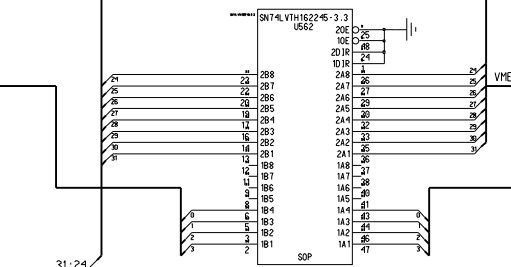


VME_etc(30:0)

Sheet 10

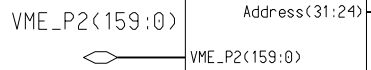
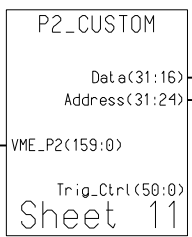


TBUSOUT(3:0)



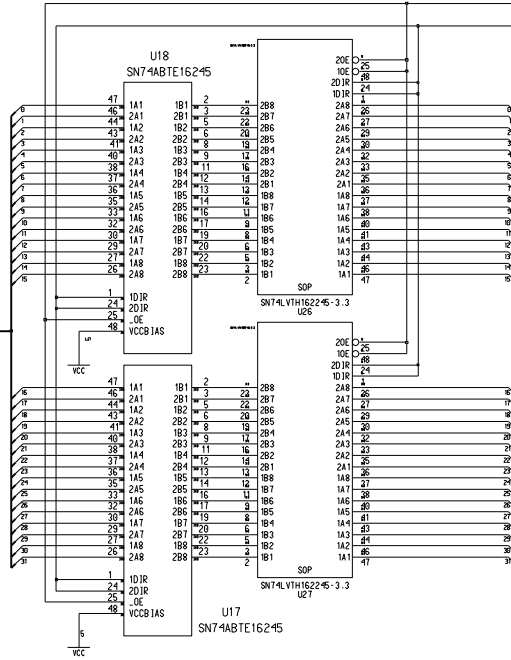
VME_Addr(31:2)

TBUSIN(3:0)



Local_VME_Data(31:0)

D(31:0)



VME_OE

VME_DIR_TRANS

Local_VME_Data(31:0)

Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Size C
DATE:	3/1/05	VME Interface 14-BIT ADC Board	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-2605
		Sheet 8 of 12	