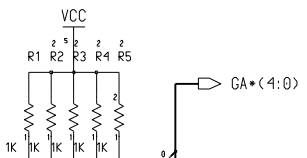
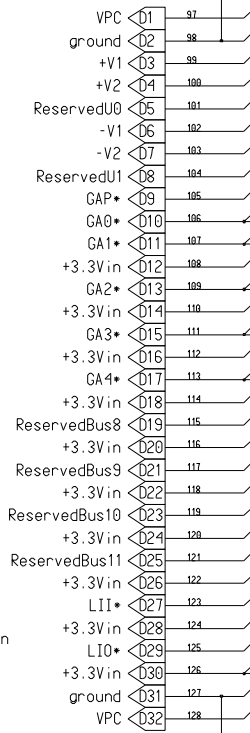
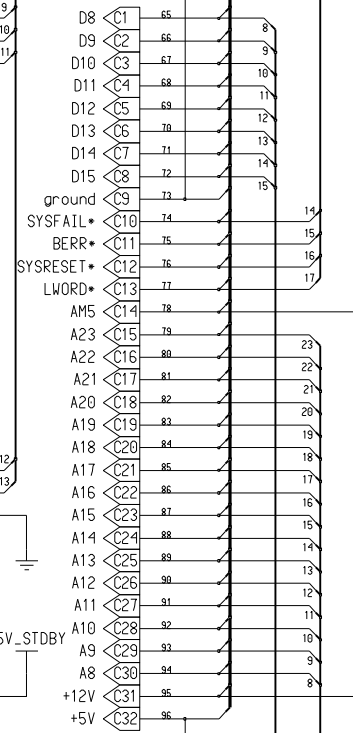
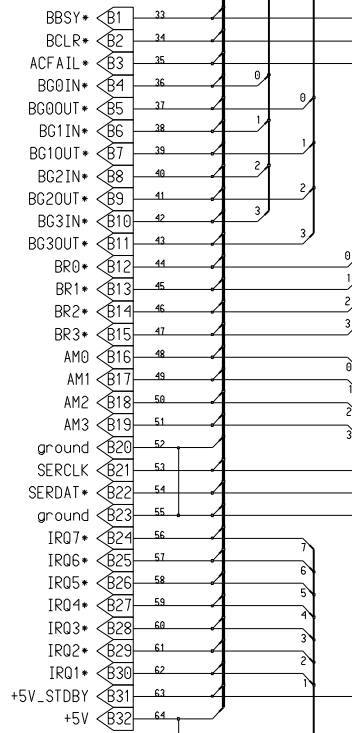
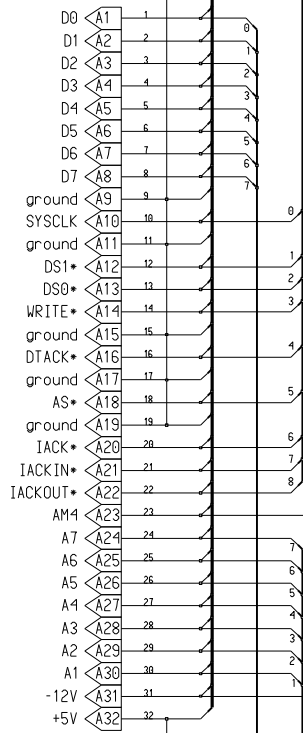
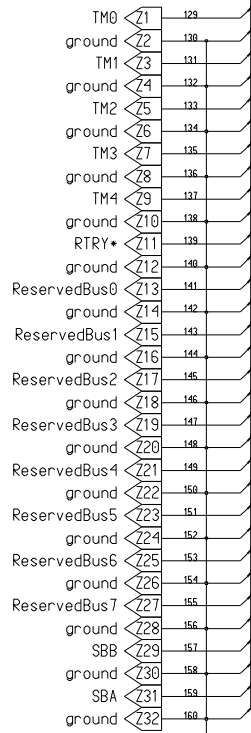


P1

VME_P1(159:0)



BGIN(3:0)
 BGOOUT(3:0)
 BR(3:0)
 VME_Control s(17:0)

GA*(4:0)

+3.3Vin

IRQ(7:1)

AM(5:0)

Address(23:1)

Data(15:0)

Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Size C
DATE:	3/1/05	VME-P1 14-BIT ADC Board	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-2605 Sheet 9 of 12