

4

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2

1

D

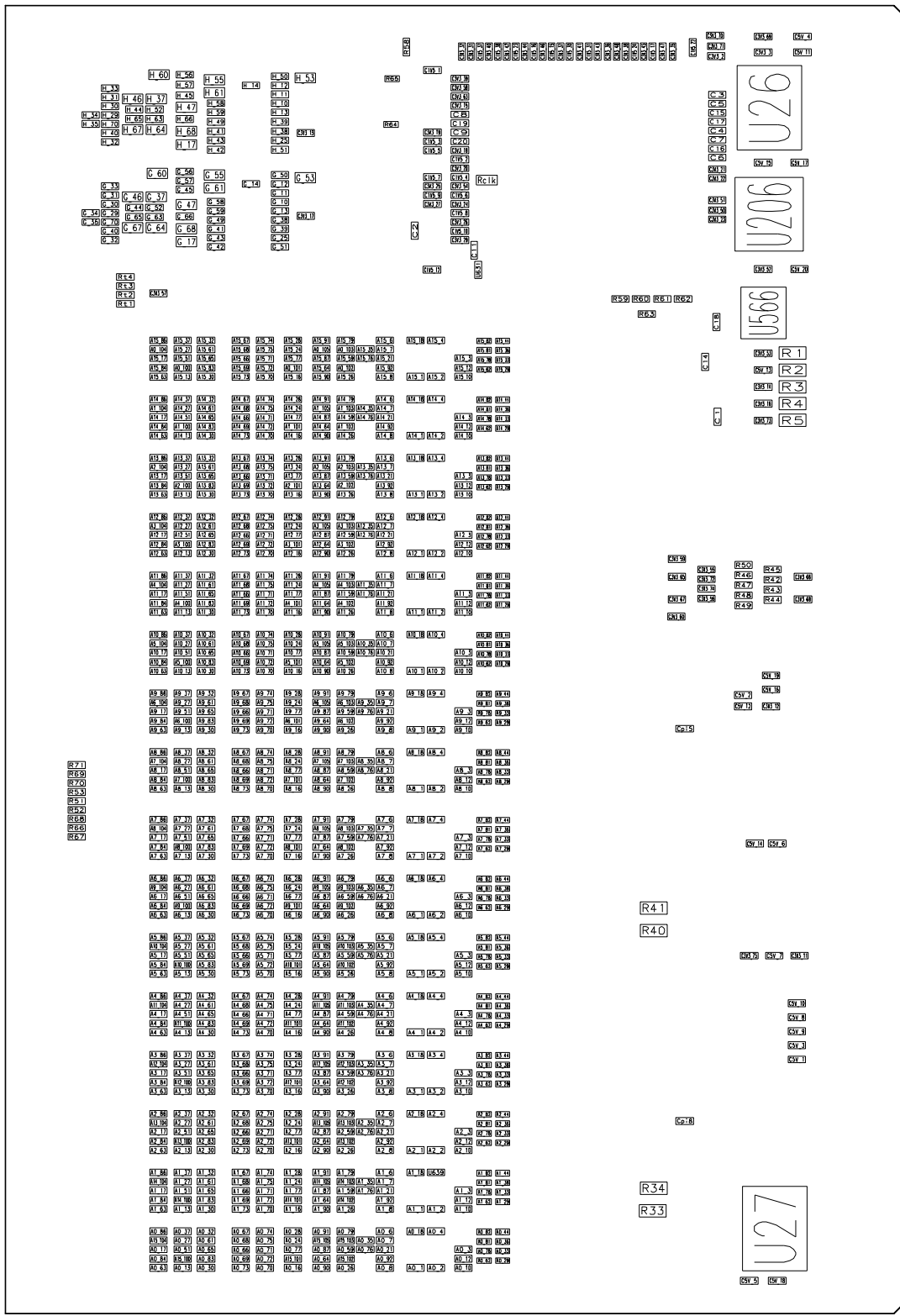
D

C

C

B

A



U26

U206

U566

R1  
R2  
R3  
R4  
R5

R41  
R40

R41  
R40

R41  
R40

R41  
R40

R41  
R40

R34  
R33

U27

Notes:

- 1. Some parts will not be installed, see BOM file.
- 3. No Centroid Data provided. Assembly house shall generate it from Gerber Files if needed.
- 4. Component Clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES . . . . . DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
TREATMENT		APPROVALS	DATE	TITLE		
FINISH		DRAWN M. Bogdan	10/30/08	14-BIT-ADC Board Assembly drawing - Bottom		
SIMILAR TO		CHECKED M. Bogdan	10/30/08	SIZE	FSCM NO.	DWG. NO.
ACT. WT	CALC. WT	ISSUED		C		B-2607-Bot
				SCALE		REV. B
				SHEET 1 of 1		

THIS SHEET IS COMPUTER GENERATED

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REV. SHEET Dwg. No.