

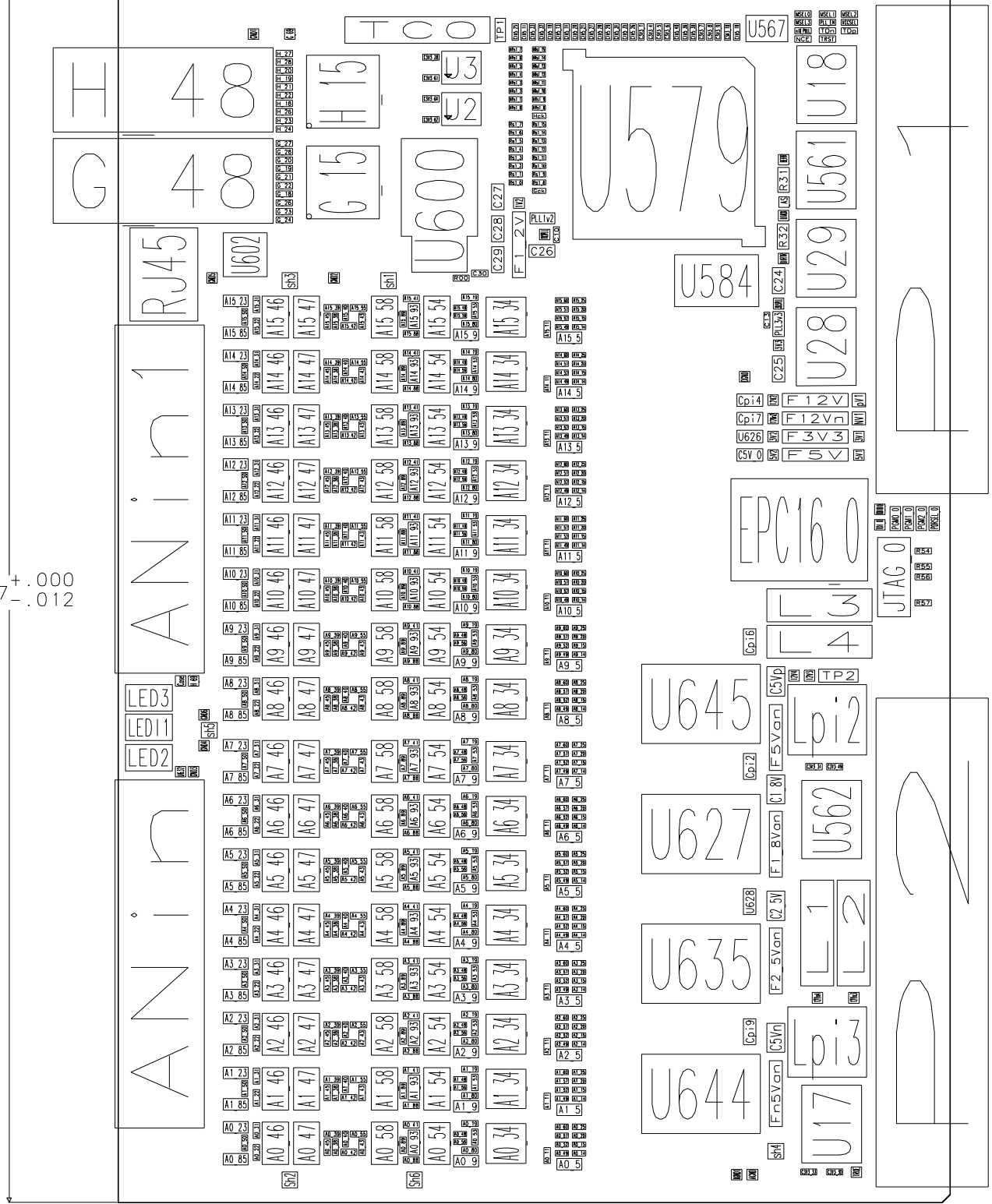
4 3 2 1

D C B A

D C B A

9.187^{+.000}
-.012

6.299^{+.000}
-.012



- Notes:
1. Some parts will not be installed, see BOM file.
 3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
 4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES XXX DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT	APPROVALS	DATE	TITLE 14-BIT-ADC Board Assembly drawing - Top				
FINISH	CHECKED	10/30/08	SIZE	FSCM NO.	DWG. NO.	REV.	
SIMILAR TO	ISSUED		C		B-2607-Top	B	
ACT. WT	SCALE	SHEET		1 of 1			

THIS SHEET IS COMPUTER GENERATED

4 3 2 1