



Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M. Bogdan	TITLE	Size C
R&D CHK		Front Panel -analog input JPARC-K 14BIT-ADC Board	
DATE:	9/9/09		
TIME:	1:30 pm	REV C	DRW. 2605
QA CHK		Sheet 4 of 12	