

D

D

C

C

B

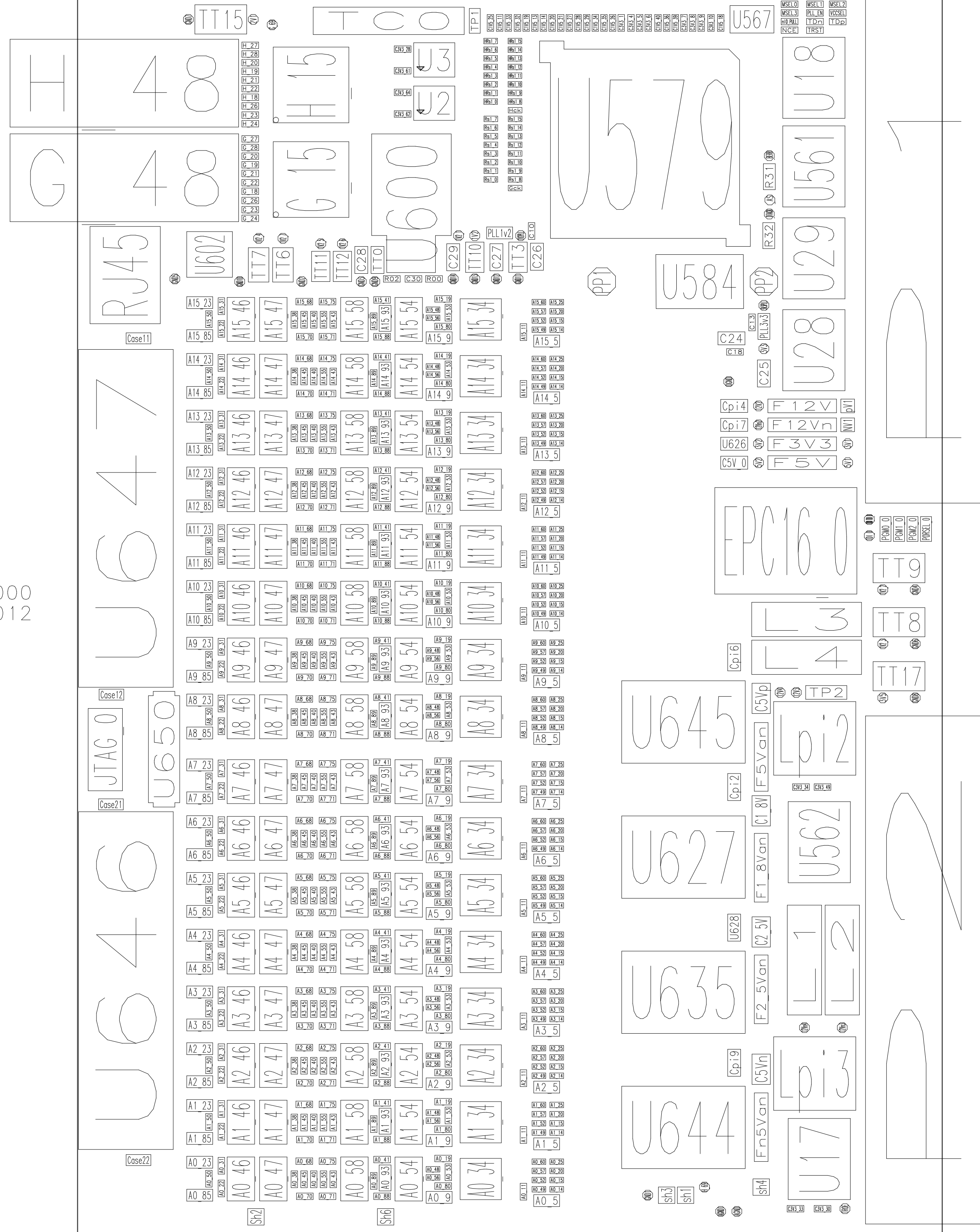
B

A

A

6.299^{+.000}_{-.012}

9.187^{+.000}_{-.012}



- Notes:
1. Some parts will not be installed, see BOM file.
 3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
 4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES .XX .XXX DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT		APPROVALS		DATE		TITLE	
FINISH		DRAWN M. Bogdan		5/11/2017		14-BIT-ADC Board Assembly drawing - Top	
SIMILAR TO		CHECKED M. Bogdan		5/11/2017		SIZE C	FSCW NO.
ACT. WT		ISSUED				DWG. NO. B-2607-Top	REV. D
CALC. WT						SCALE	SHEET 1 of 1

DWG. NO. REV. SHEET