

1

2

3

4

A

A

B

B

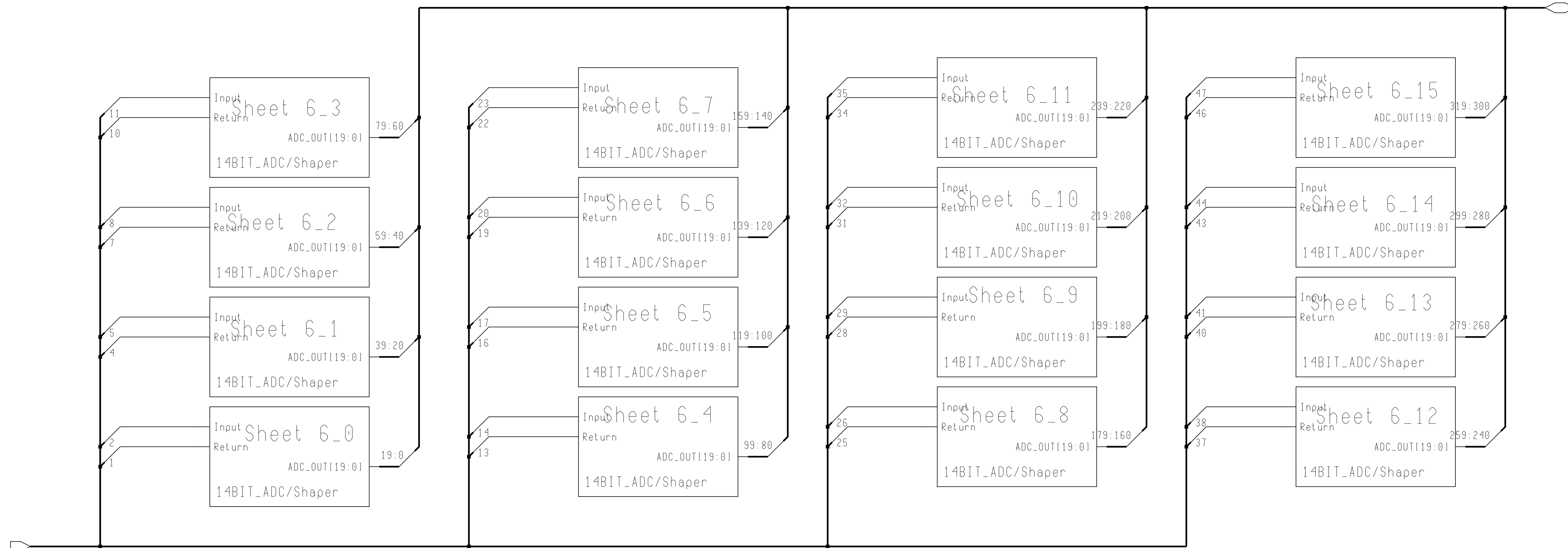
C

C

D

D

16ADCs_IO(319:0)



FP1(47:0)

Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M. Bogdan		
R&D CHK		TITLE	Size
DATE:	4/23/08	ADC Channels 0	
TIME:	2:00 pm	JPARC-K 14BIT-ADC Board	
QA CHK		REV B	DRW. B-2606 Sheet 5 of 12

1

2

3

4