

4

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1

D

C

B

A

D

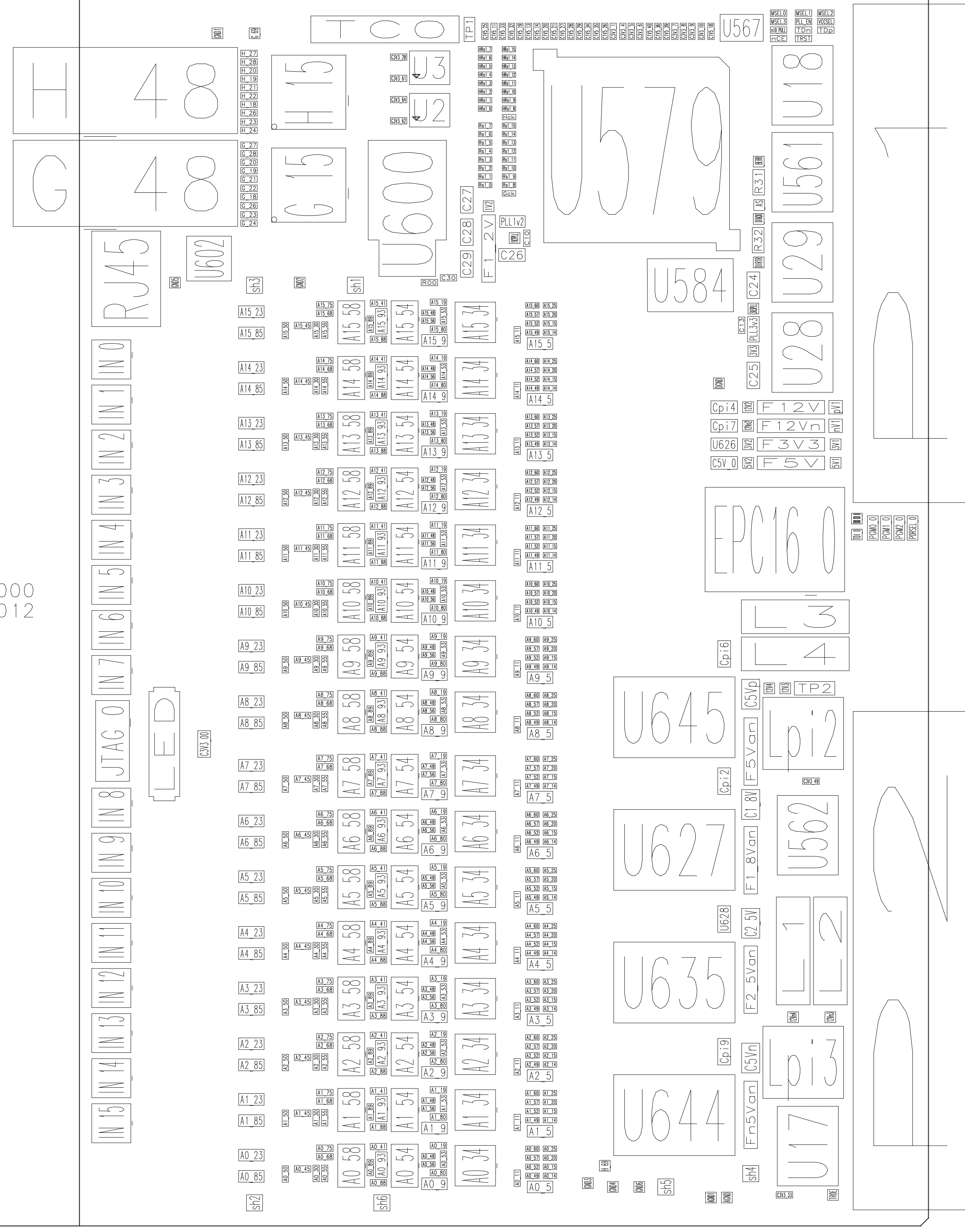
C

B

A

6.299^{+0.000}
-0.012

9.187^{+0.000}
-0.012



Notes:

- 1. Some parts will not be installed, see BOM file.
- 3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
- 4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES .XX DO NOT SCALE DRAWING	CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
	APPROVALS	DATE	TITLE		
TREATMENT	DRAWN M. Bogdan	1/21/15	14-BIT-ADC Board Assembly drawing - Top		
FINISH	CHECKED M. Bogdan	1/21/15	SIZE C	FSCN NO.	DWG. NO. B-2607-Top
SIMILAR TO	ACT. WT	CALL WT	ISSUED	SCALE	REV. LOMO
					SHEET 1 of 1

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