



**Notes:**

*This schematic is for testing the Preamp/Shaper/ADC\_Driver circuit for the 14-Bit, 125MHz ADC Board.*

*The actual schematic will have the +1V and -1V levels generated from the ADC chip reference output.*

*This circuit is designed to receive negative pulses, the gain is adjustable.*

*The values: L1,C2,L3,C4,L5,C6,L7 in the 7-pole filter determine the FWHH for the output pulse.*

Engineer	M. Bogdan	TITLE	Preamp/Shaper Test
Drawn by	M. Bogdan		
DATE: 4/18/07	REV A	DRW.B-2606-T	Sheet 6-T of