



- Notes:
1. Some parts will not be installed, see BOM file.
 3. No Centroid Data provided. Assemblt house shall generate it from Gerber Files if needed.
 4. Component Clearance: 0.3 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS .XXX DECIMALS .XX ANGLES .XX DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
TREATMENT		APPROVALS	DATE	TITLE		
FINISH		DRAWN M. Bogdan	6/29/2016	1,500MHz ADC Module Assembly drawing - Bottom		
SIMILAR TO		CHECKED M. Bogdan	6/29/2016	SIZE C	FSCM NO.	DWG. NO. 2858- Bot
ACT. WT	CALC WT	ISSUED	SCALE	REV. A	SHEET 1 of 1	