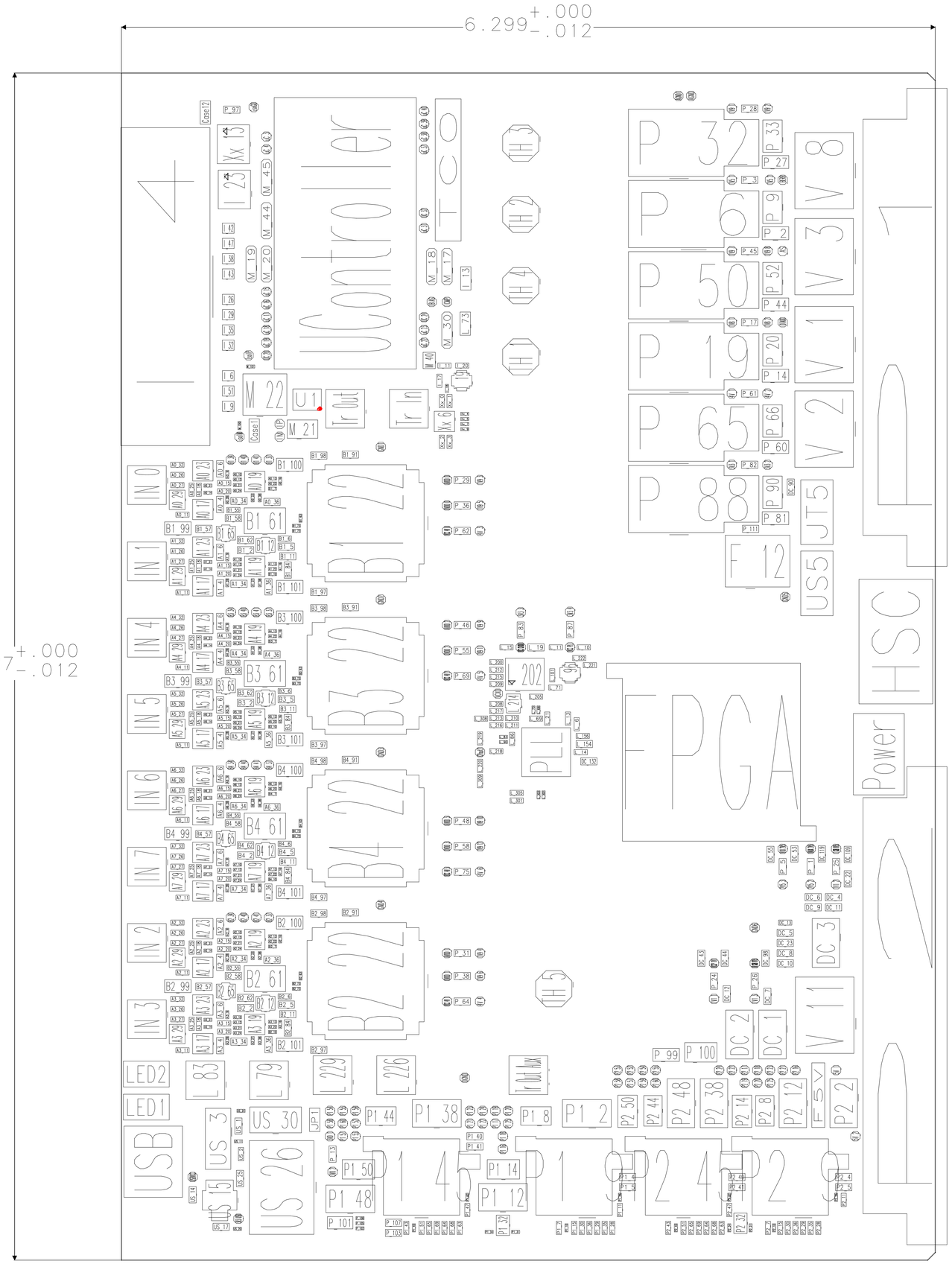

Notes:

1. Some parts will not be installed, see BOM file.

3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
4. Component clearance: 0.3 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX .XXX DO NOT SCALE DRAWING	CONTRACT NO.	UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TREATMENT	APPROVALS / DRAWN M.Bogdan	DATE 6/29/2016	TITLE 1,500MHz ADC Assembly drawing - Top
FINISH	CHECKED M.Bogdan	DATE 6/29/2016	SIZE C / FSCM NO. / DWG.NO. 2870 - Top / REV. A
SIMILAR TO / ACT.WT / CALC WT	ISSUED		SCALE / SHEET 1 of 1

THIS SHEET IS COMPUTER GENERATED