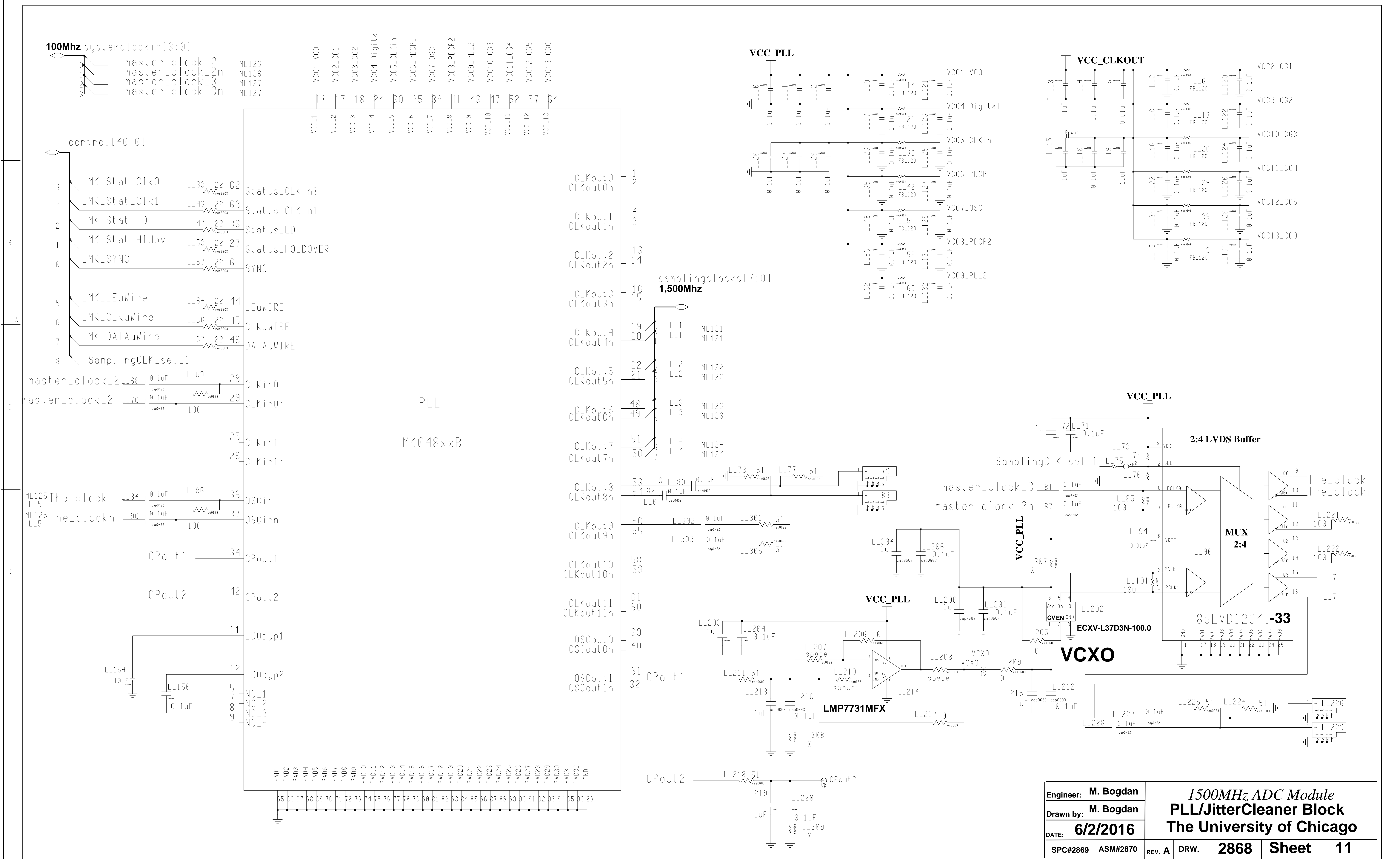


Max 3dB Bandwidth: 2,400MHz



Engineer: M. Bogdan	1500MHz ADC Module PLL/Jitter Cleaner Block The University of Chicago		
Drawn by: M. Bogdan			
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