



VME_P1(159:0)

VMEbus_P1
ReservedBus(7:0)
Data(15:0)
Address(23:1)
AM(5:0)
BR(3:0)
BGIN(3:0)
BGOUT(3:0)
IRQ(7:1)
GA(4:0)
VME_Controls(17:0)

VME_P2(159:0)

P2_CUSTOM
Data(31:16)
Address(31:24)
Trig_Ctrl(50:0)

VME_etc(30:0)

VME_Addr(31:2)

VME_OE

VME_DIR_TRANS

Local_VME_Data(31:0)

Engineer: M. Bogdan	1500MHz ADC Module VME Interface The University of Chicago
Drawn by: M. Bogdan	
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