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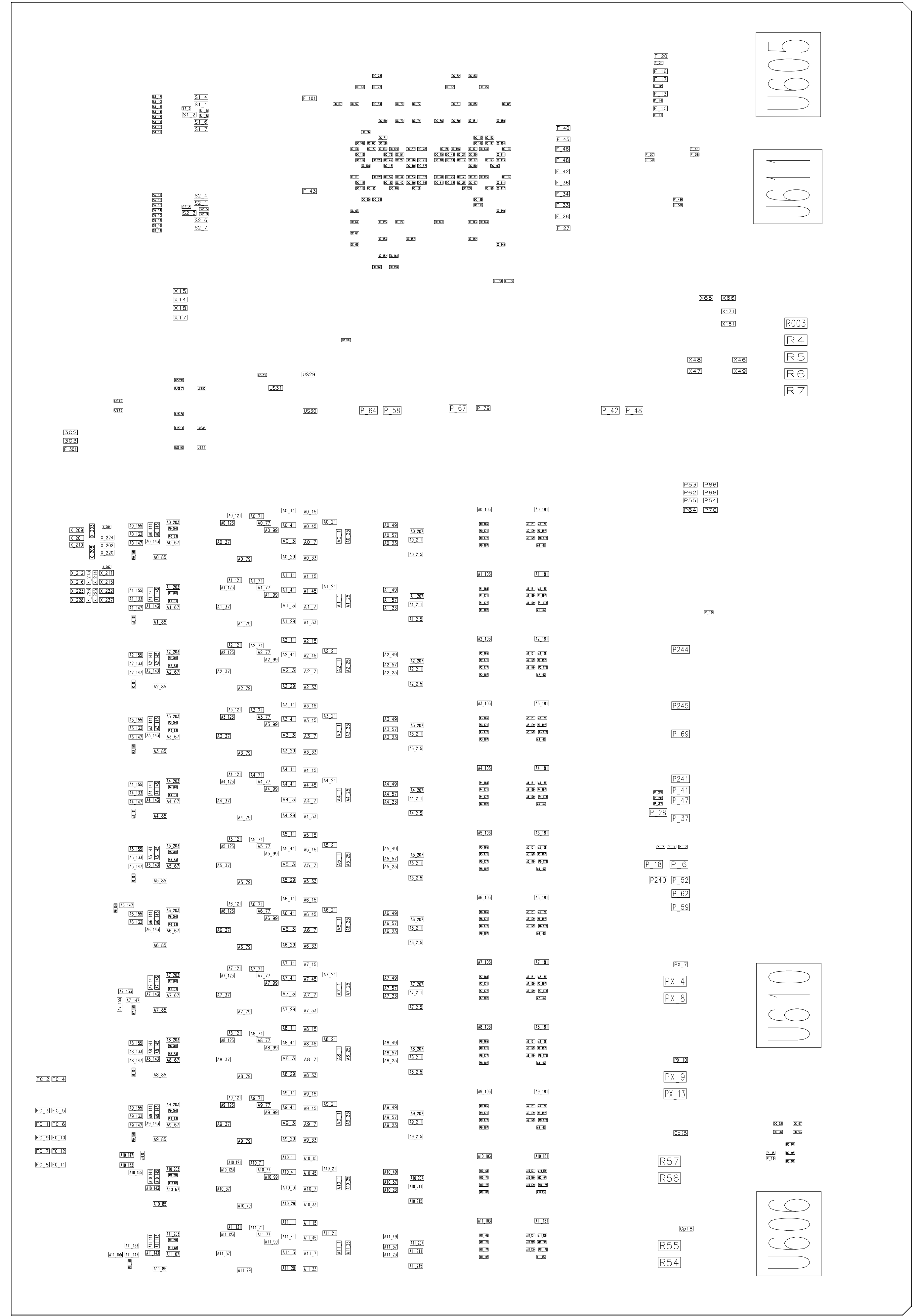
C

C

B

A

A



Notes:

1. Some parts will not be installed, see BOM file.
3. No Centroid Data provided. Assemblt house shall generate it from Gerber Files if needed.
4. Component Clearance: 0.3 mm.

| | | | | | |
|---|---------|---------------------|--|--|----------|
| UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX .XXX DO NOT SCALE DRAWING | | CONTRACT NO. | | UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP | |
| APPROVALS | | DATE | | TITLE | |
| DRAWN M. Bogdan | | 4/15/2016 | | 20-BIT ADC Assembly drawing - Bottom | |
| CHECKED M. Bogdan | | 4/15/2016 | | SIZE C | FSCM NO. |
| ISSUED | | Dwg. No. 2867 - Bot | | REV. A | |
| SIMILAR TO | ACT. WT | SCALE | | SHEET 1 of 1 | |

THIS SHEET IS COMPUTER GENERATED

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