



Notes:

1. Some parts will not be installed, see BOM file.
3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .00 .01 .02 .05 .10 .15 .20 .30 .40 .50 .60 .70 .80 .90 1.00 .00 .01 .02 .05 .10 .15 .20 .30 .40 .50 .60 .70 .80 .90 1.00 .00 .01 .02 .05 .10 .15 .20 .30 .40 .50 .60 .70 .80 .90 1.00 DO NOT SCALE DRAWING		CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP			
TREATMENT		APPROVALS		DATE		TITLE	
FINISH		DRAWN M. Bogdan		8/10/16		20-BIT ADC Tester Assembly drawing - Top	
SIMILAR TO		CHECKED M. Bogdan		8/12/16		SIZE FSCW NO. DWG. NO. 2882 REV. A	
ACT. WT. CALC. WT.		ISSUED		SCALE		SHEET 1 of 1	

DWG. NO. REV. SH