

4

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D

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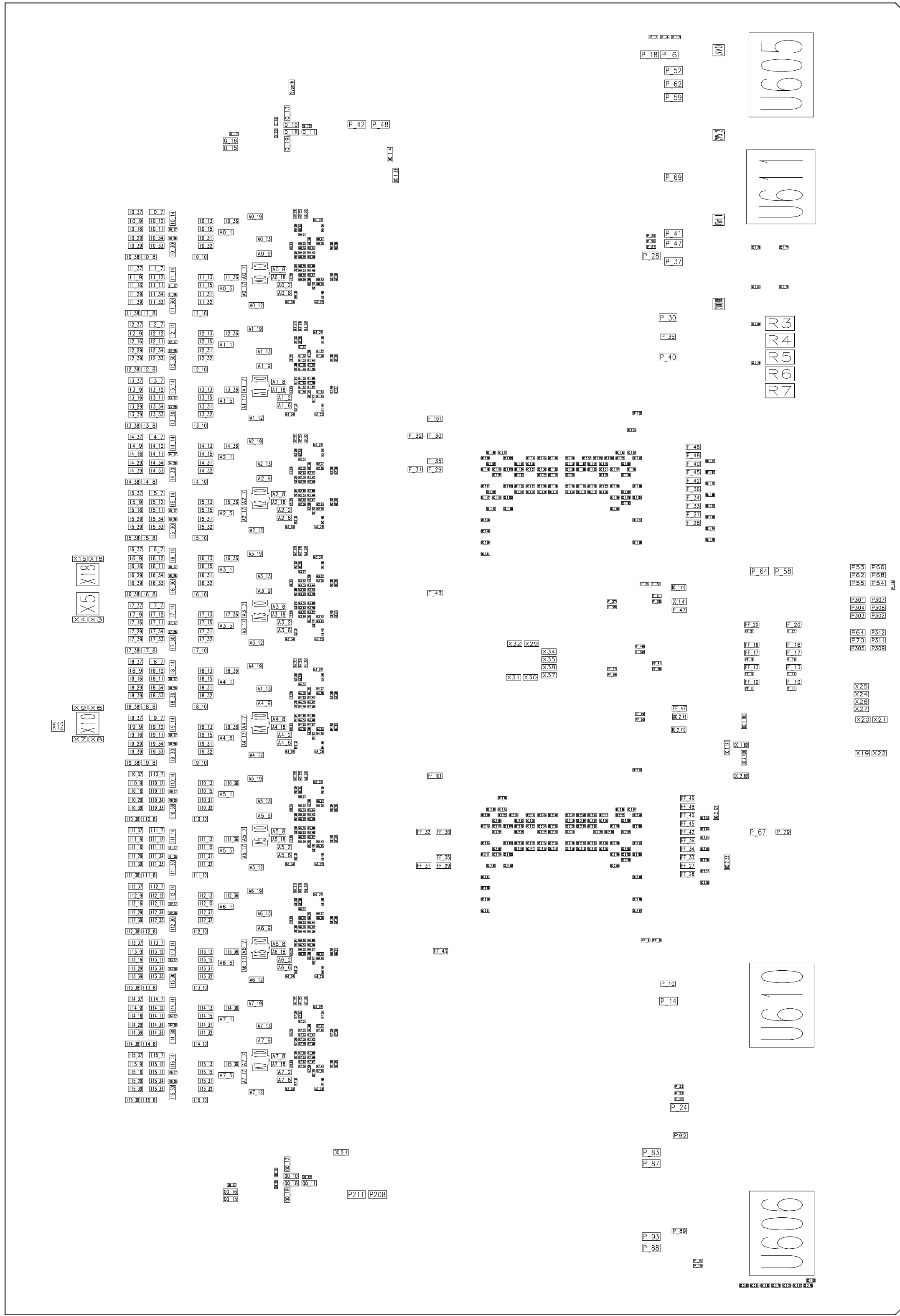
C

C

B

A

A



Notes:

- 1. Some parts will not be installed, see BOM file.
- 3. No Centroid Data provided. Assemblt house shall generate it from Gerber Files if needed.
- 4. Component Clearance: 0.3 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX DO NOT SCALE DRAWING	CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
	APPROVALS DRAWN M. Bogdan	DATE 10/10/14	TITLE 500MHz ADC Board Assembly drawing - Bottom		
TREATMENT	CHECKED M. Bogdan	DATE 10/10/14	SIZE C	FSCM NO.	DWG. NO. 2837- Bot
FINISH	ISSUED		SCALE		REV. A
SIMILAR TO	ACT. WT	CALC WT			SHEET 1 of 1

THIS SHEET IS COMPUTER GENERATED

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DWG. NO. REV. SHEET