

1

2

3

4

A

A

B

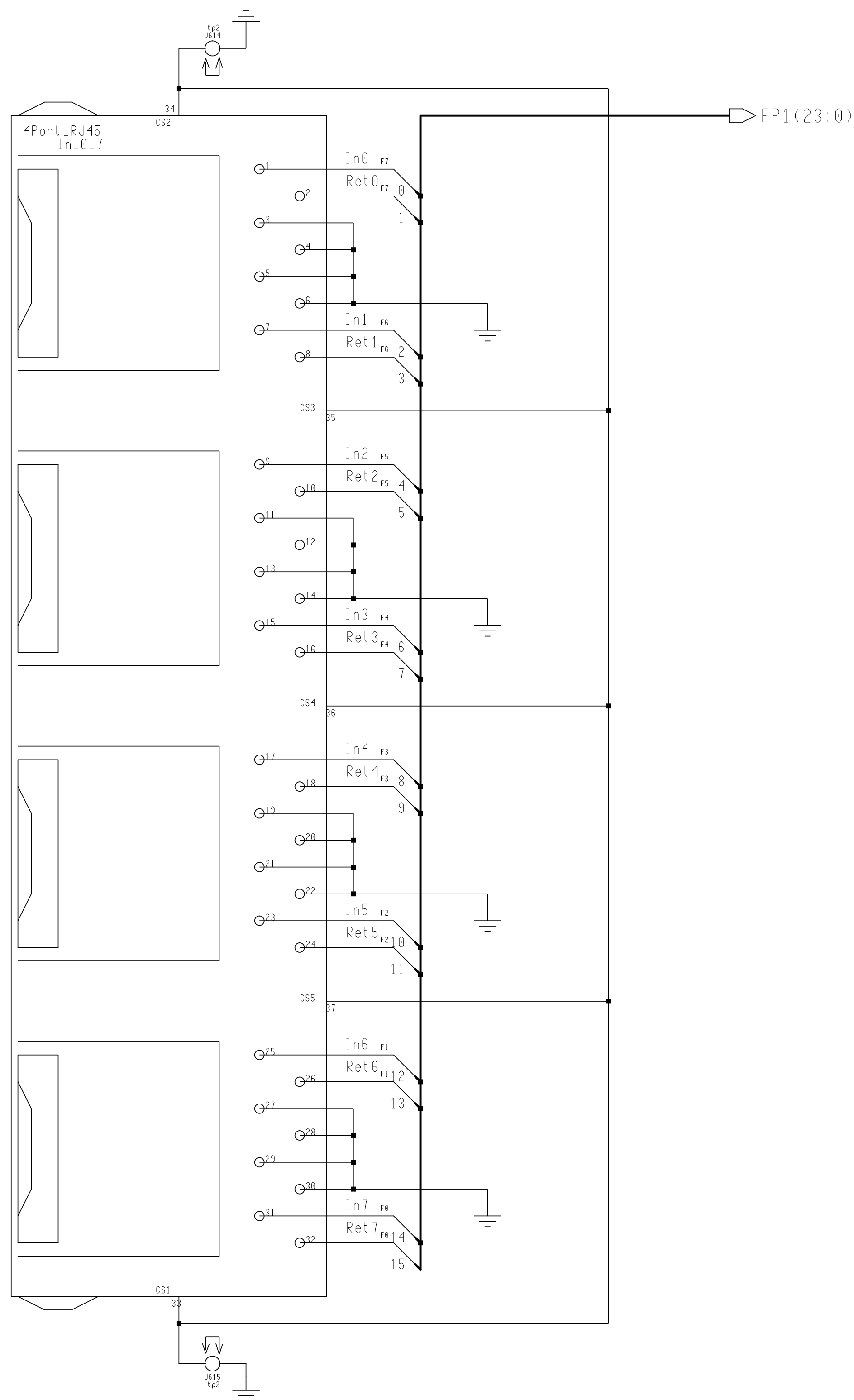
B

C

C

D

D



Engineer: **M. Bogdan**

Drawn by: **M. Bogdan**

DATE: **10/9/2014**

The University of Chicago

Analog Input Connector: 0-7

500MHz 16Ch ADC Module

REV. A

DRW.

2835

Sheet

3_0

1

2

3

4