



Notes:  
The components marked "space" shall not be installed.

Engineer: <b>M. Bogdan</b>	<b>The University of Chicago</b> ADC Channel 0 and 1 500MHz 16 Ch ADC Module
Drawn by: <b>M. Bogdan</b>	
DATE: <b>10/9/2014</b>	
REV. A   DRW. <b>2835</b>   Sheet <b>4_0</b>	