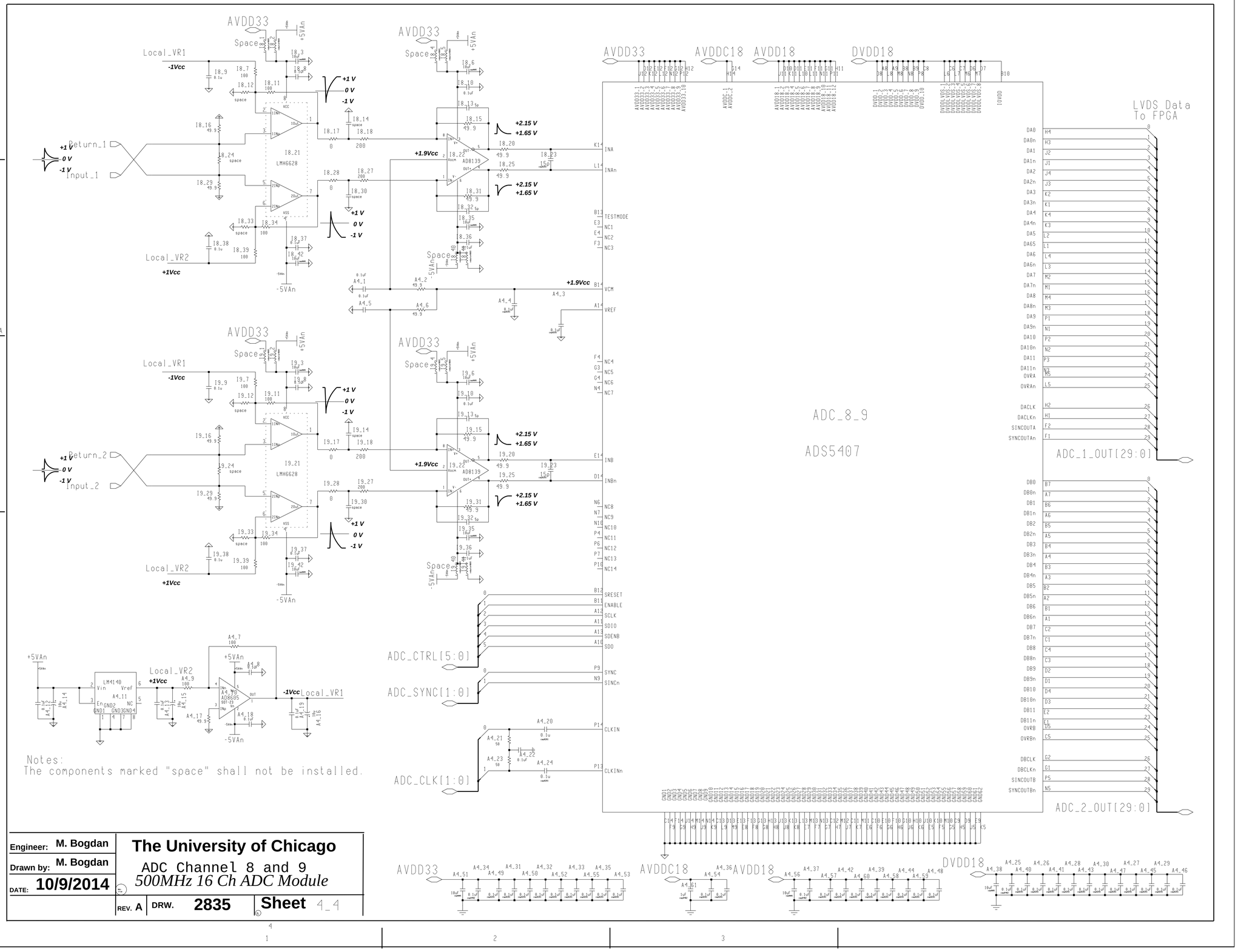

Engineer: M. Bogdan	The University of Chicago		
Drawn by: M. Bogdan	ADC Channel 8 and 9		
DATE: 10/9/2014	*500MHz 16 Ch ADC Module*		
	REV. A	DRW. 2835	Sheet 4_4