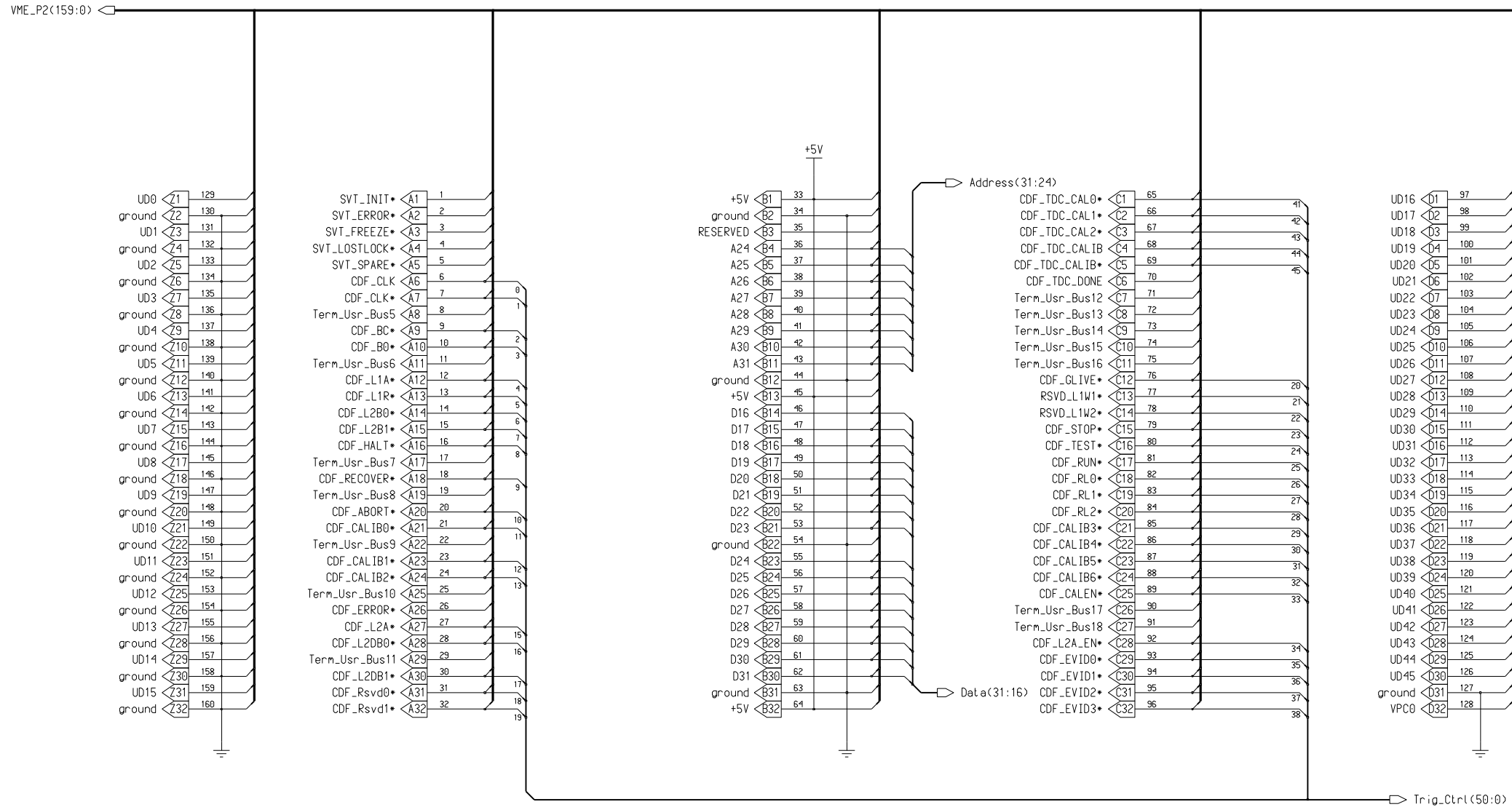


P2



|          |           |   |                            |
|----------|-----------|---|----------------------------|
| Engineer | M. Bogdan | The University of Chicago               |                            |
| Drawn by | M. Bogdan | 5640 S. Ellis Ave.<br>Chicago, IL 60637 |                            |
| R&D CHK  |           | TITLE                                   | Size<br>B                  |
| DATE:    | 11/17/09  | <b>VME-P2<br/>500MHz ADC</b>            |                            |
| TIME:    | 2:00 pm   |   |                            |
| QA CHK   |           | REV<br>A                                | DRW. 2664   Sheet 11 of 12 |