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| Engineer | M. Bogdan | The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637 | |
| Drawn by | M. Bogdan | | |
| R&D CHK | | | |
| DATE: | 11/17/09 | TITLE | Size B |
| TIME: | 2:00 pm | Power 500MHz ADC | |
| QA CHK | | REV A | Sheet 12 of 12 |