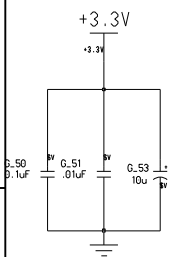
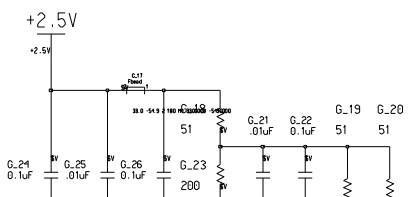
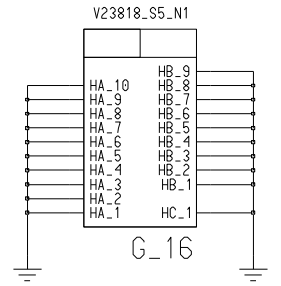
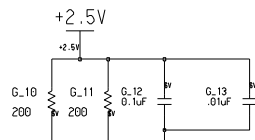
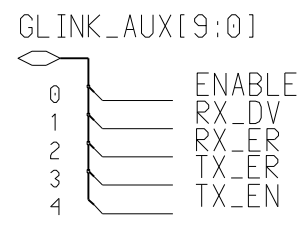
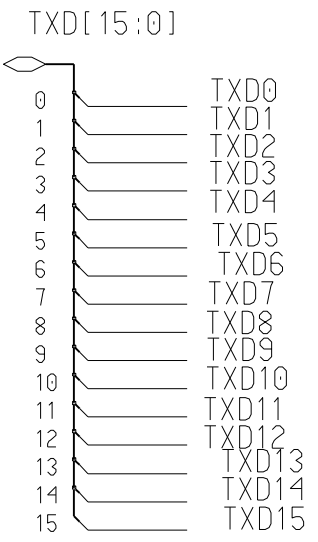
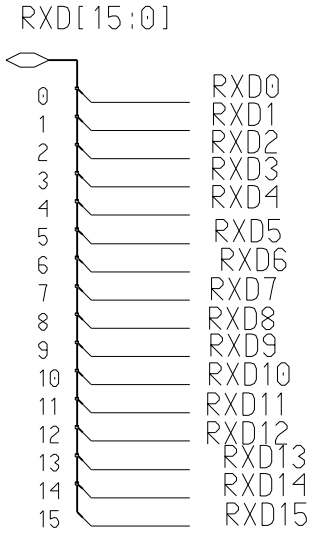
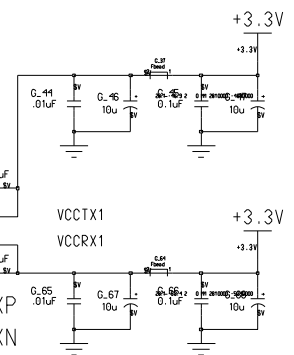


XCLK2 \xrightarrow{Gck} 33 $\xrightarrow{}$ XCLK



DINRXP
DINRXN

DTXP
DTXN



Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M. Bogdan		
R&D CHK		TITLE	Size B
DATE:	11/17/09	Optical Link Block 500MHz ADC	
TIME:	2:00 pm		
QA CHK		REV A	DRW. 2664 Sheet 5-1 of 12