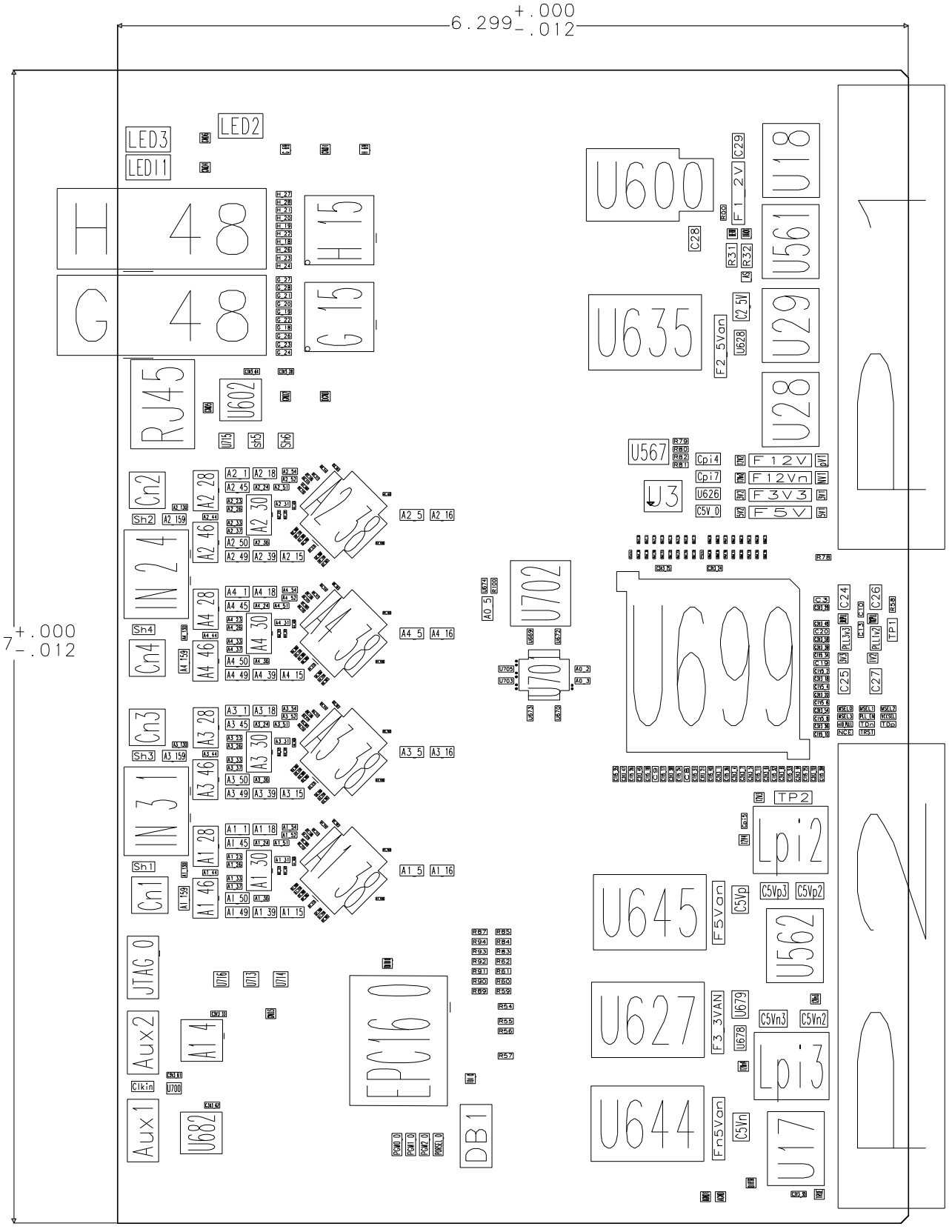

Notes:

1. Some parts will not be installed, see BOM file.

3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX .XXX DO NOT SCALE DRAWING	CONTRACT NO.	UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP
	APPROVALS / DATE	TITLE: 500MHz ADC Board Assembly drawing - Top
TREATMENT	DRAWN M.Bogdan 10/10/10	
FINISH	CHECKED M.Bogdan 10/10/10	SIZE C · FSCM NO. · DWG. NO. 2666 - Top · REV. B
SIMILAR TO · ACT. WT · CALC WT	ISSUED	SCALE · SHEET 1 of 1

THIS SHEET IS COMPUTER GENERATED

REV. SH DWG. NO.