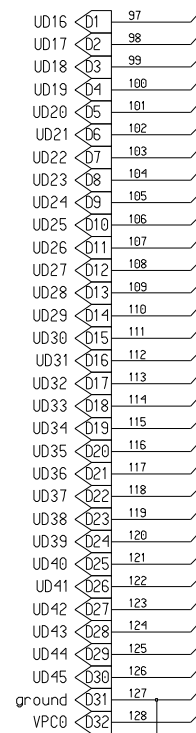
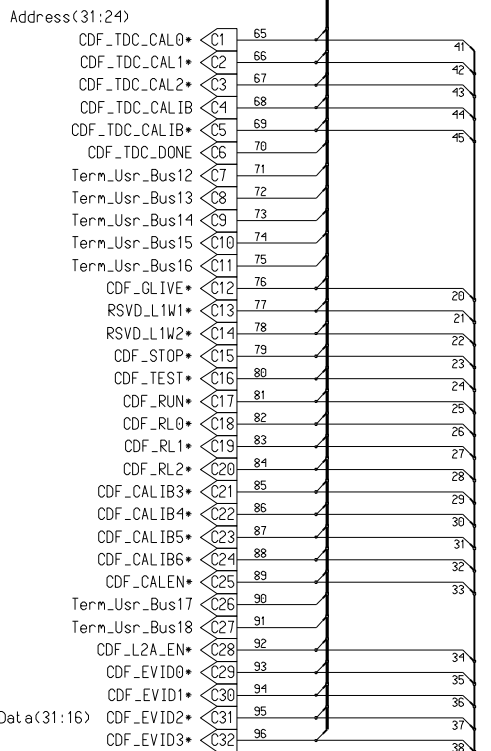
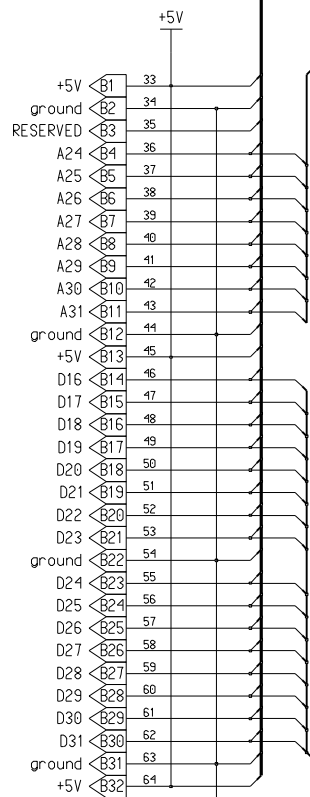
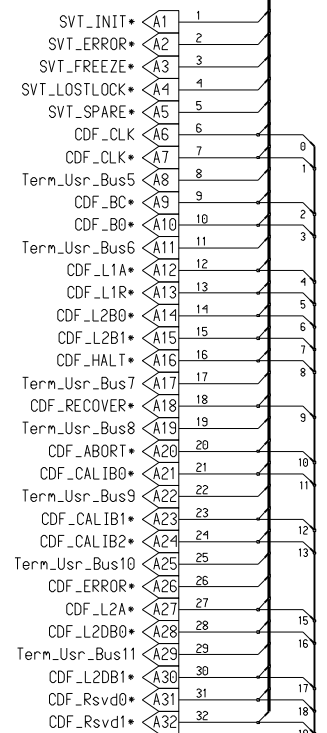
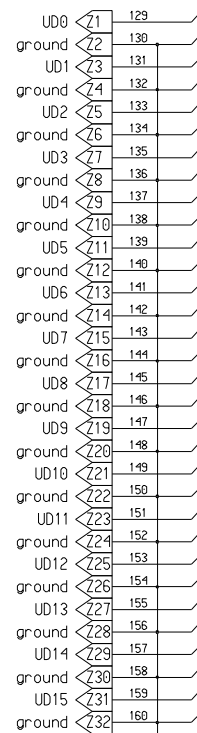


P2

VME_P2(159:0)



Trig_Ctrl(50:0)

Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Size B
DATE:	11/17/09	VME-P2 500MHz ADC	
TIME:	2:00 pm		
QA CHK		REV A	DRW. 2664 Sheet 11 of 12