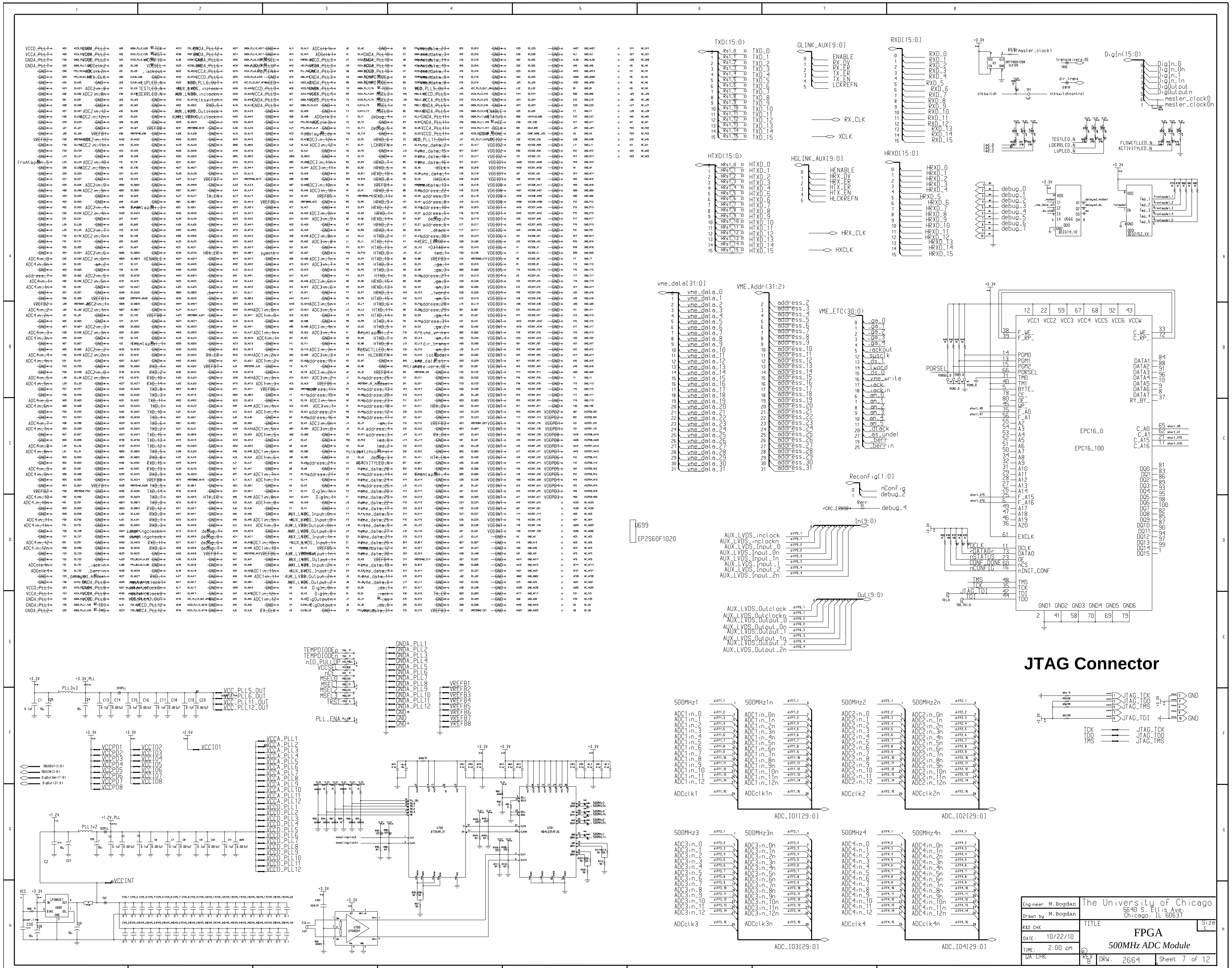
JTAG Connector
EP2S60F1020
Engineer M.Bogdan
Drawn by M.Bogdan
R&D CHK
DATE: 10/22/10
TIME: 2:00 pm
QA CHK
The University of Chicago
5640 S. Ellis Ave.
Chicago, IL 60637
TITLE
FPGA
500MHz ADC Module
Size C
REV B
DRW. 2664
Sheet 7 of 12