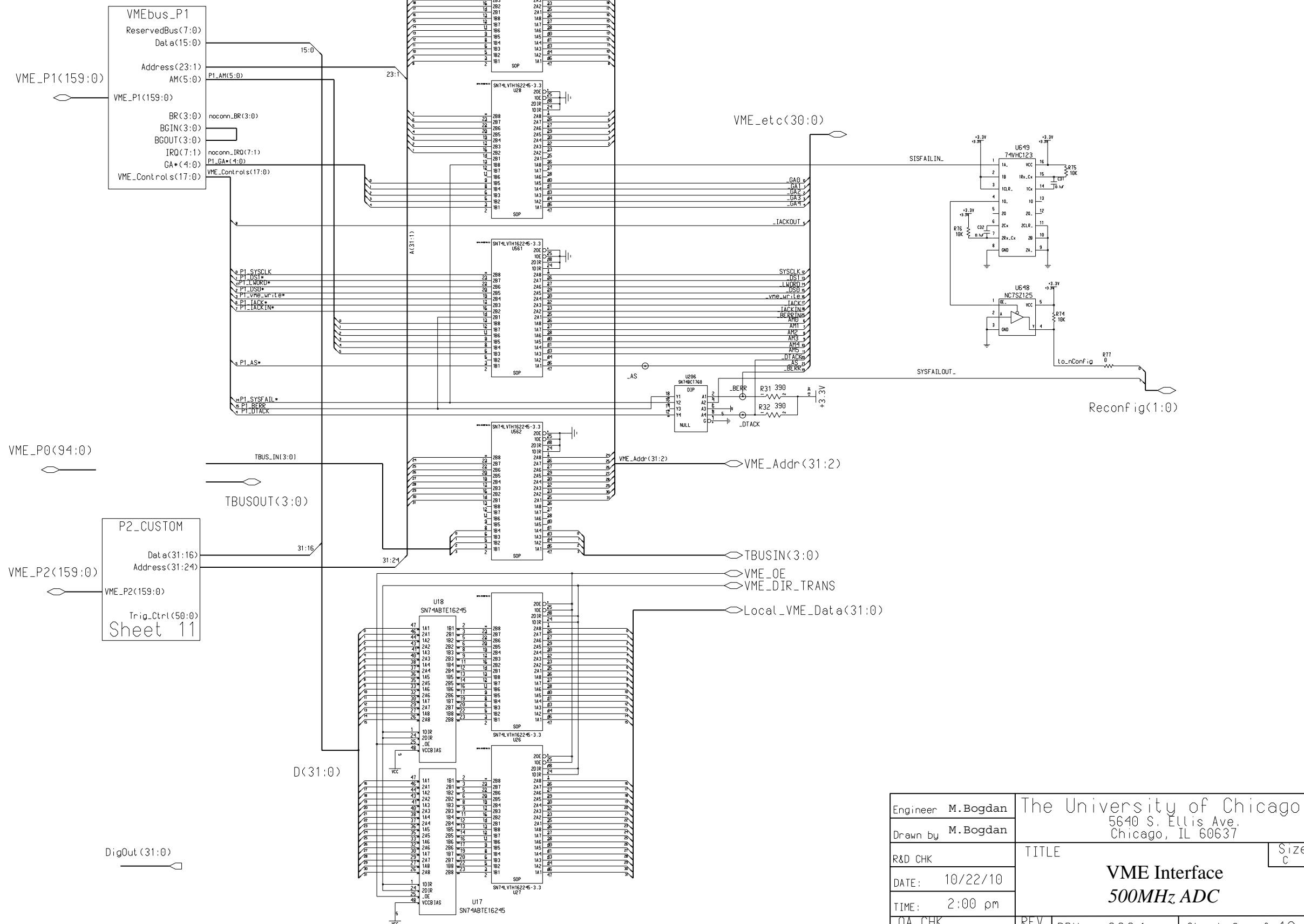


Sheet 9



Sheet 11

Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Size c
DATE:	10/22/10	VME Interface 500MHz ADC	
TIME:	2:00 pm		
QA CHK		REV B	DRW. 2664 Sheet 8 of 12