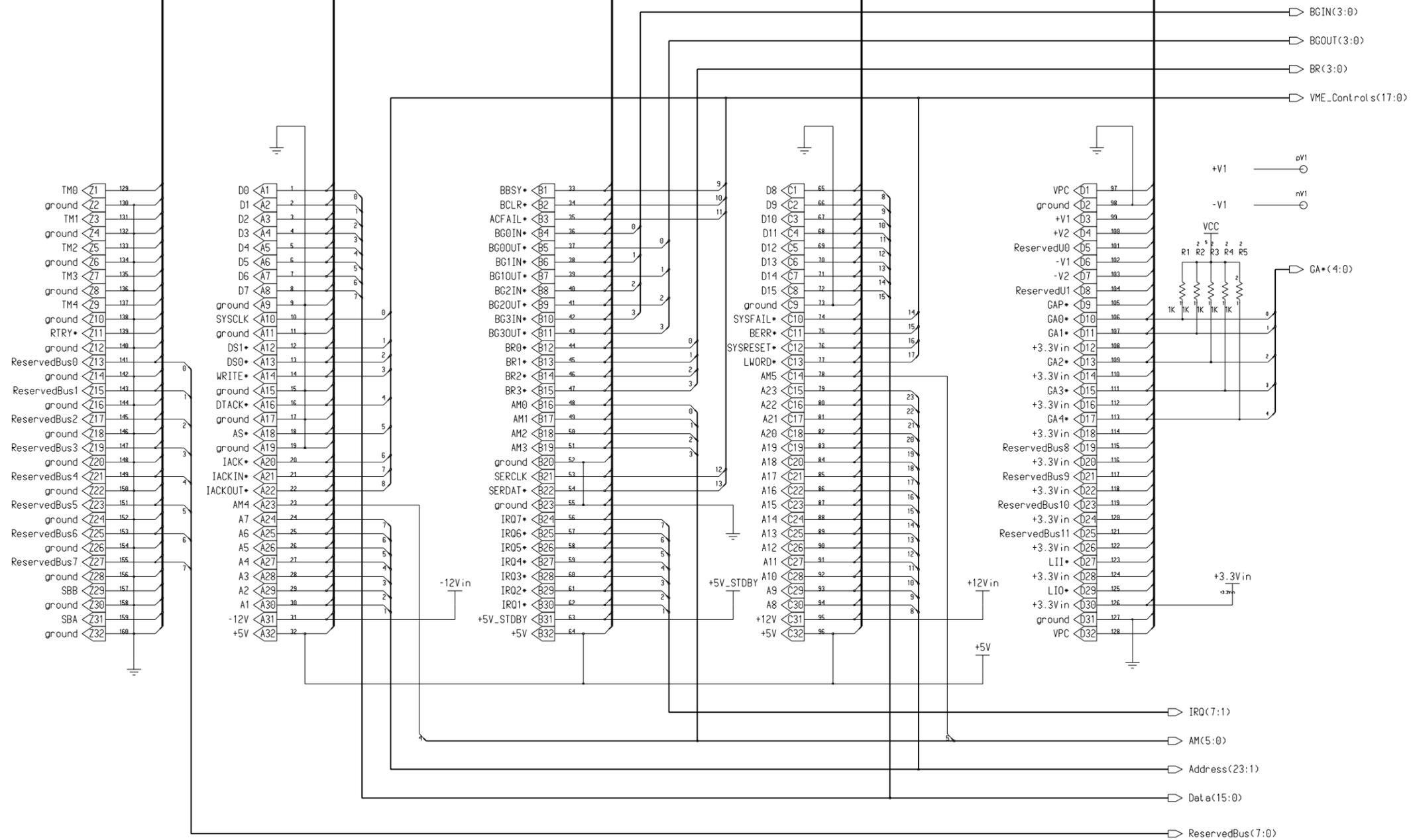


# P1

VME\_P1(159:0)



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R&D CHK		TITLE	Size C
DATE:	10/19/09	<b>VME-P1 500MHz ADC</b>	
TIME:	2:00 pm		
QA CHK		REV A	DRW. 2664 Sheet 9 of 12