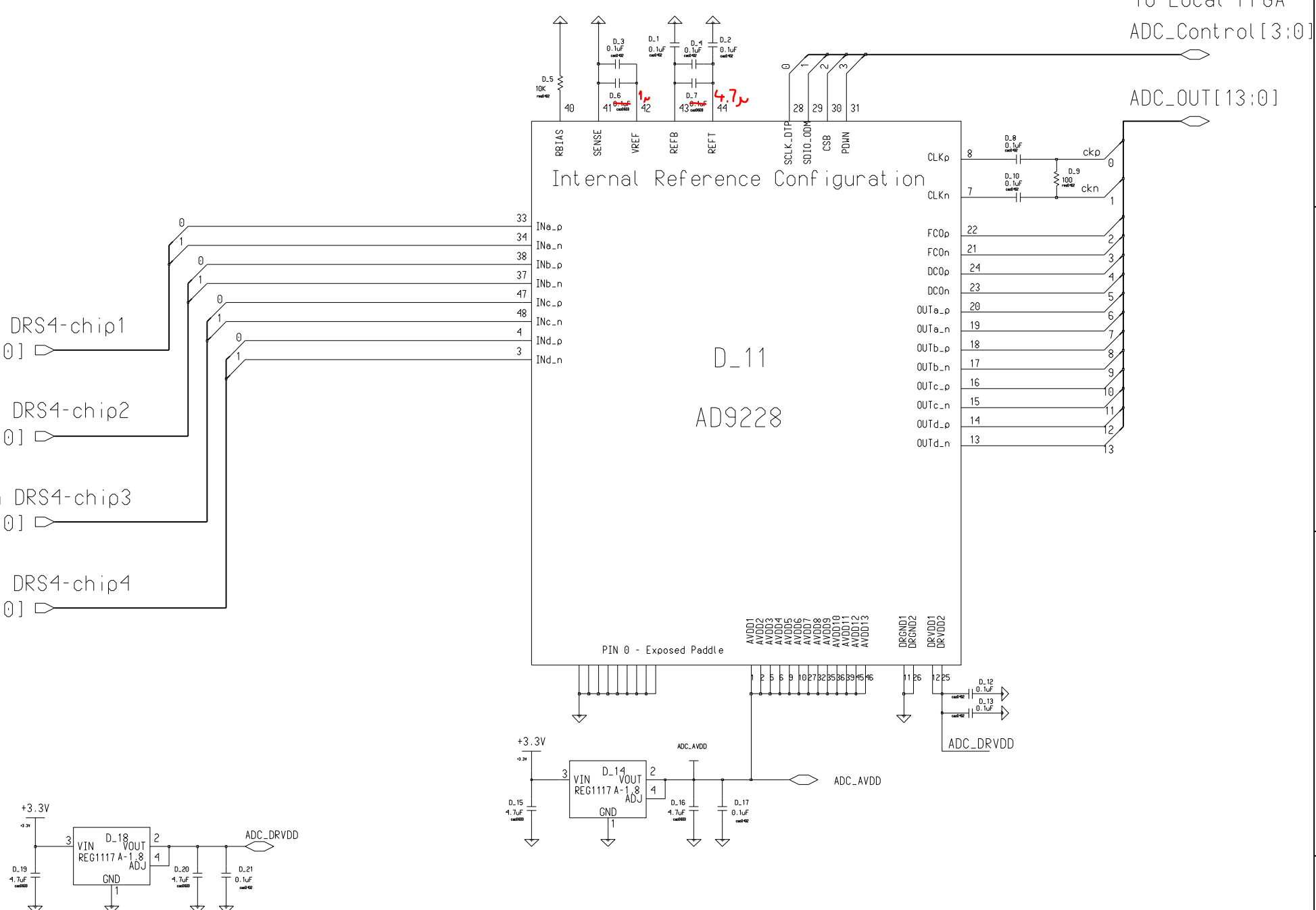


8th channel from DRS4-chip1  
Input1[1:0]

8th channel from DRS4-chip2  
Input2[1:0]

8th channel from DRS4-chip3  
Input3[1:0]

8th channel from DRS4-chip4  
Input4[1:0]



Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637		
Drawn by	M. Bogdan			
R&D CHK		<b>Aux ADC Block</b> <b>1GSPS ADC Module</b>		
DATE:	5/26/09			
TIME:	2:00 pm			
QA CHK		REV A	DRW. 2644	Sheet 1-7