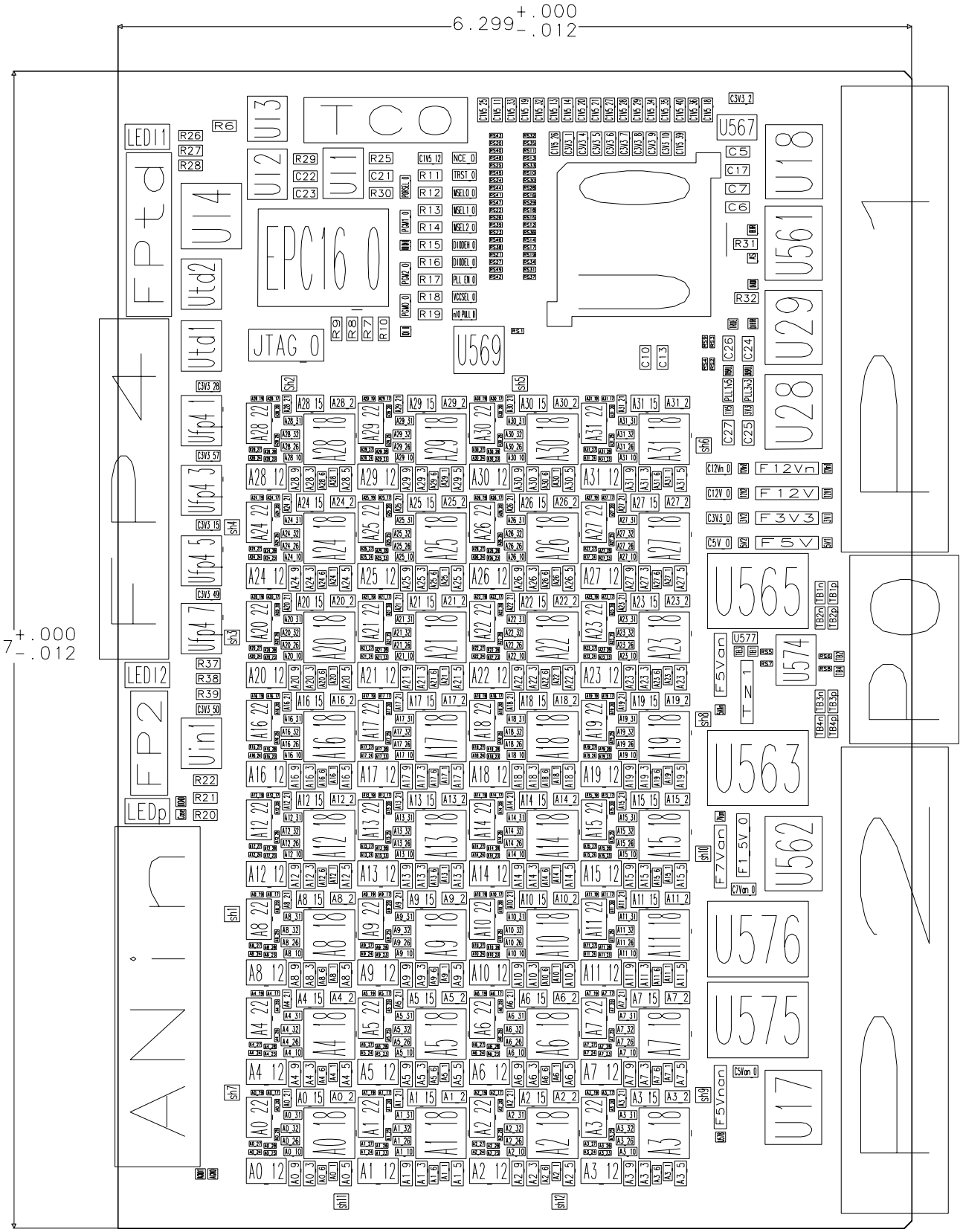

Notes:

1. Some parts will not be installed, see BOM file.
2. No solder on or under P0.
3. No Centroid data provided. Assembly house shall generate it from Gerber Files if needed.
4. Component clearance: 0.4 mm.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS .XX .XXX ANGLES DO NOT SCALE DRAWING	CONTRACT NO.	UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
	APPROVALS / DATE	TITLE QUIET_ADC Board Assembly drawing - Top	
TREATMENT	DRAWN M.Bogdan 3/14/05		
FINISH	CHECKED M.Bogdan 3/14/05	SIZE C / FSCM NO. / DWG. NO. B-2562-Top	REV. A
SIMILAR TO / ACT. WT / CALC WT	ISSUED	SCALE / SHEET 1 of 1	

THIS SHEET IS COMPUTER GENERATED