Notes:
1. This is the schematic of one ADC Channel. 32 identical channels on board (6U VMX).
2. Each ADC Channel covers an area of 28mm x 22.5mm.
3. GND - Digital ground plane - one for the whole board.
4. AGROUND - Analog ground plane - one for each ADC Channel (32 AGROUND planes total).
5. Each AGROUND plane connects to the GND plane under the AD7674 chip.
6. +3.3V digital power supply from the crate, referenced to GND.
7. +5V analog power, one for whole board, referenced to GND, linearly regulated from crate's +12V.
8. Each ADC Channel connects independently to the one FPGA on board.
9. The ADCs work at 800kHz sampling rate, 16-bit, serial interface, slave mode, external clock.

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ADC Channel j
QUIET_ADC

The University of Chicago
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Title: ADC Channel j

Engineer: M. Bogdan

Drawn by: M. Bogdan

Date: 09/17/05
Time: 11:00 am

Account: 12

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