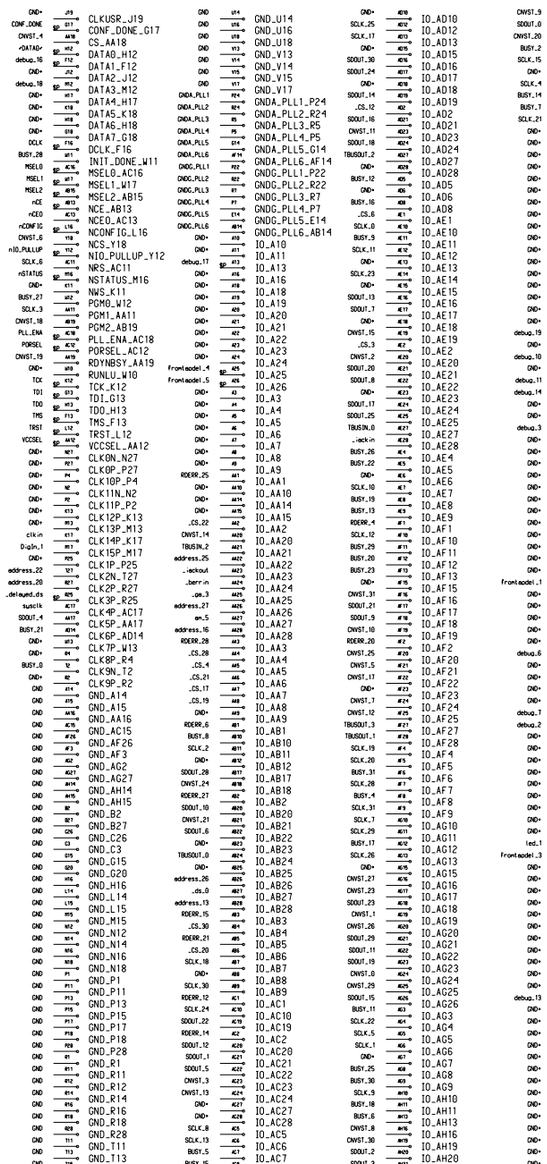

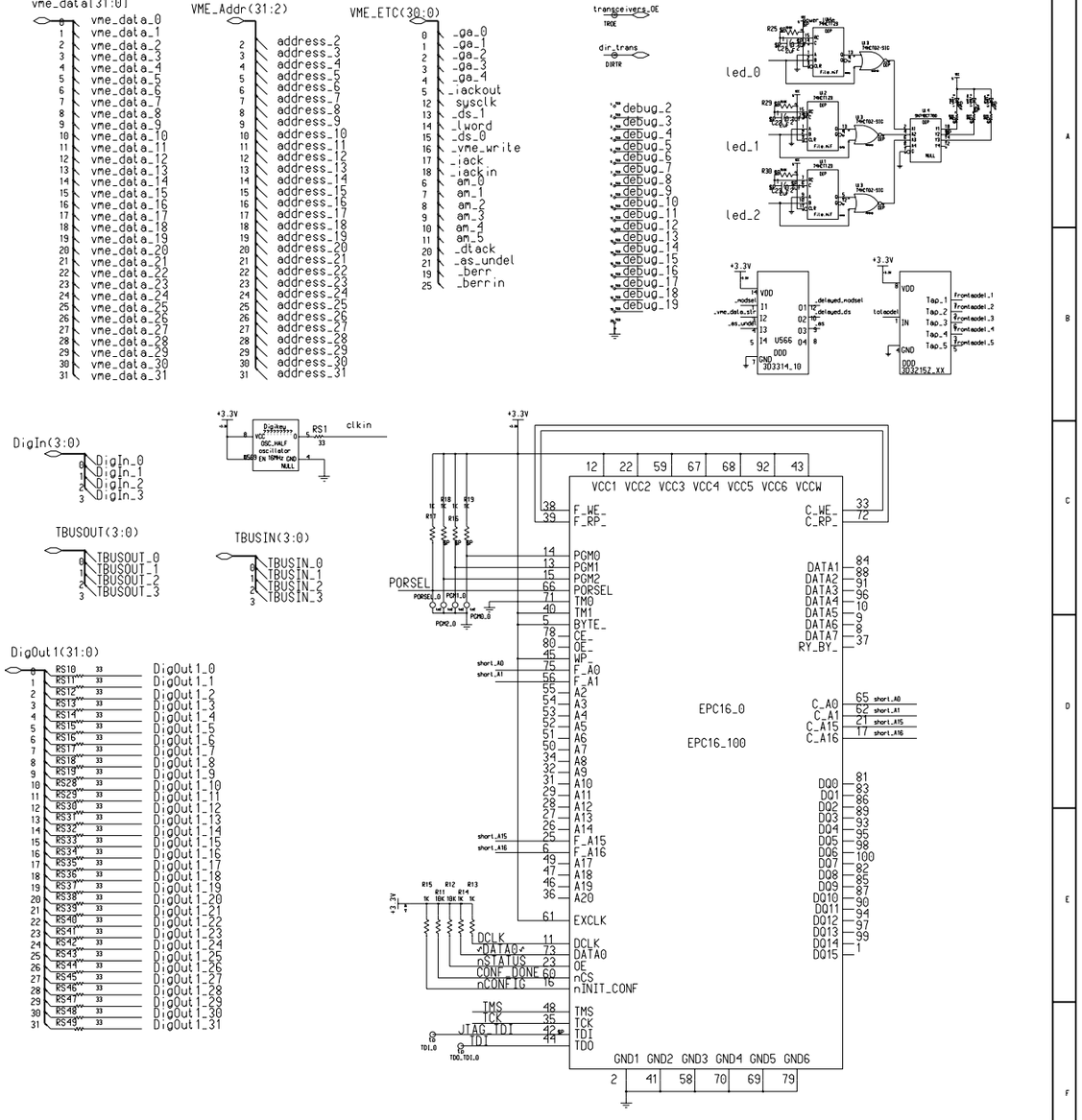

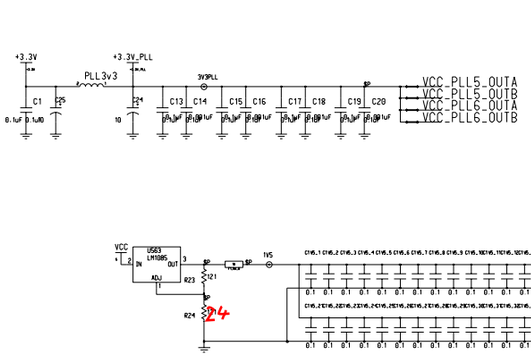

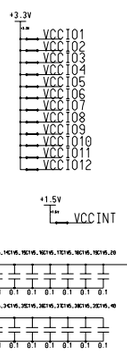

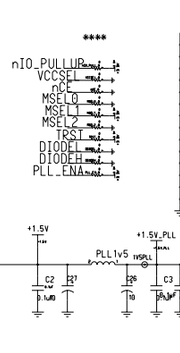

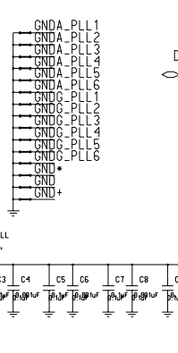

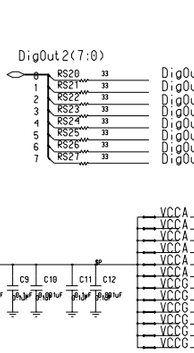

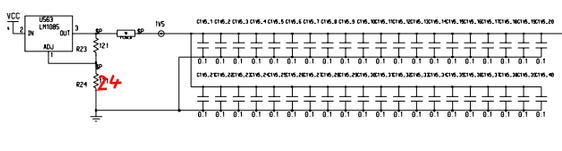

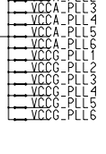

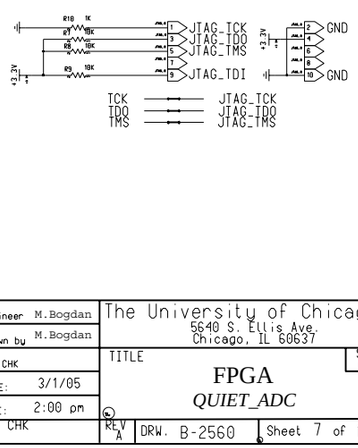

Engineer	M.Bogdan
Drawn by	M.Bogdan
R&D CHK	
DATE:	3/1/05
TIME:	2:00 pm
QA CHK	

The University of Chicago
5640 S. Ellis Ave.
Chicago, IL 60637

TITLE: FPGA
QUIET_ADC

Size C

REV A | DRW. B-2560 | Sheet 7 of 12