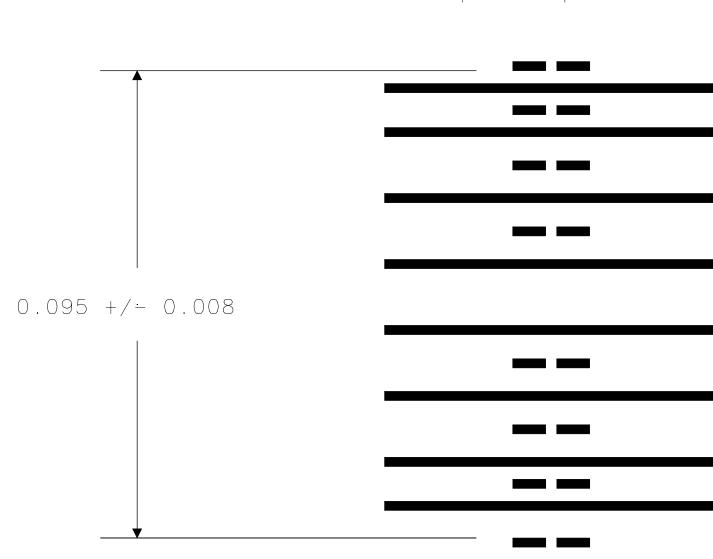


THIS SHEET IS COMPUTER GENERATED

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| ∑ DWG.NO. 2857 SH REV.A | $\neg$ | _ |         |                              |    |      |  |
|-------------------------|--------|---|---------|------------------------------|----|------|--|
| $\checkmark$            |        |   | DWG.NO. | 2857                         | SH | REV. |  |
|                         |        | / |         | $\angle \bigcirc \bigcirc /$ |    |      |  |



Boar

- 1. M 2. M 3. M 4. 1
- 5. E

- 6. Bc 7. Mi 8. Si
- 9.4 10.F
- 11. 12.
- P€ P' 13. V
- р
- 14.
- 15.

| DWG.NO. 2857   | SH REV.  |  |                           |             |
|--|--|--|---------------------------|-------------|
|  | Top – Comp.Side  | Layer Order  |                           |             |
|  |  | 1.Signal_1, Top<br>2.Ground<br>3.Signal_2<br>4.Power<br>5.Signal_3<br>6.Power<br>7.Signal_10<br>8.Power                                      |                           |             |
| 0.095 +/- 0.008  |  | 9.Power<br>10.Signal_11<br>11.Power<br>12.Signal_7<br>13.Ground <sup>-</sup><br>14.Signal_8<br>15.Ground <sup>-</sup><br>16.Signal_6, Bottom |                           |             |
| rd Characteristics - 16 LA<br>Naterial: Nelco N4000-13EPS<br>Ninimum trace width: 0.006"<br>Ninimum trace width and cle<br>oz copper for all power<br>/2 oz copper for Stripline   | and clearence: 0.005<br>arence: 0.005" on Signal   | " on Signal_1,6 (Top and Bot<br>nal_2,3,4,5,7,8,9,10,11,12 (<br>_1,2 (Top and Bottom)<br>_2,3,4,5,7,10,11,12).                               | tom);<br>(all stripline); |             |
| Apply Solder Mask over bare<br>board Thickness: 0.093 +/-<br>lill the Top and Bottom of<br>silkscreen on Component and<br>5 degree chamfer.<br>THS tolerances: +/- 0.003 u<br>nterlayer spacing as speci<br>Cc=55 Ohm, Zd=100 Ohm for co<br>Perform TDR test for all si<br>Present TDR test for all si<br>Present TDR test results for<br>lia Fill and Overplate:<br>/ias of this diameter must<br>blanarized, and plated over<br>The plated cap must adhere | e copper.<br>0.008<br>board on the solder s<br>Solder Sides.<br>unless specified other<br>fied.<br>all 0.005" stripline a<br>ignal layers.<br>or all signal layers.<br>be completely filled<br>with Copper and surf<br>to fill material afte | with Peters PP-2795 or equi<br>face finish.<br>er 1x 550F solder shock.  | " +/- 0.008<br>[aces.     | B           |
| Remove all non-functional<br>Do not increas size of the  |  | pins and vias.<br>ted spoke connections on 0.(   | )41" and 0.0413" hol€     | es.         |
| DIMENSIONS ARE IN INCHES<br>TOLERANCES ARE:<br>FRACTIONS DECIMALS ANGLES<br>. XX<br>. XXX<br>DO NOT SCALE DRAWING<br>TREATMENT<br>FINISH   | APPROVALS DATE TITLE<br>M.Bogdan 9/30/2015   | Annie's Centra<br>Specification  | MENT GROUP<br>Card        | $\bigwedge$ |
| SIMILAR TO ACT. WT CALC WT   | B<br>Scal  |  | ET                        |             |