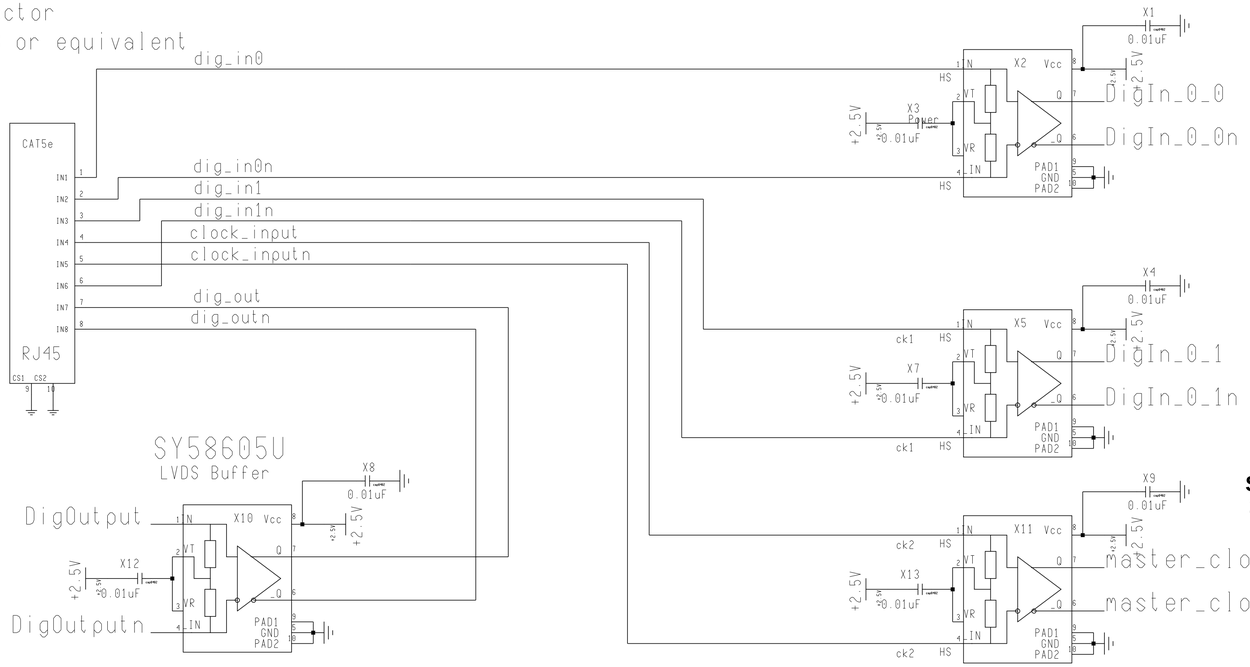
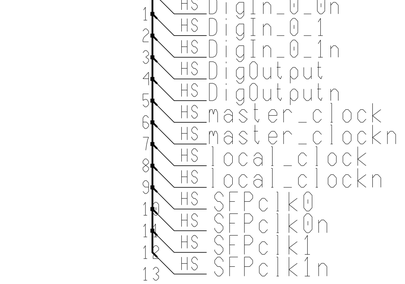
RJ45 Connector
AJT65B8813 or equivalent

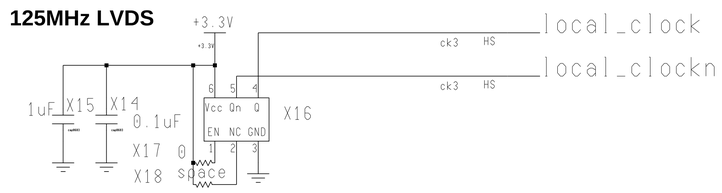


System Reference Clock
125MHz LVDS



ECS-LVDS33-1250-BN

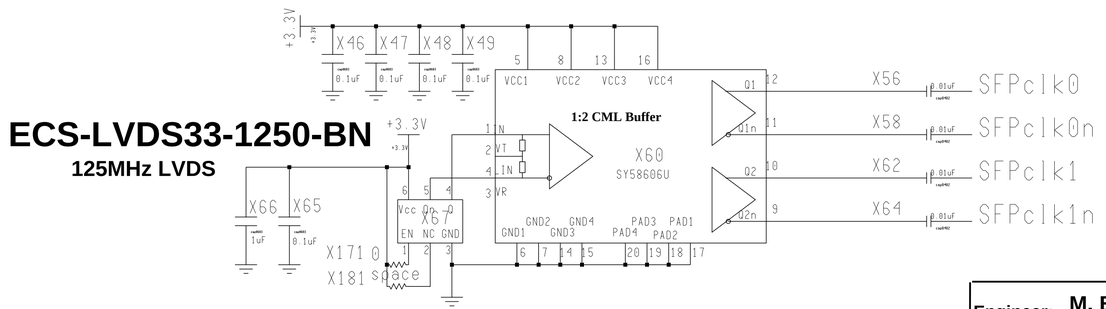
125MHz LVDS



Internal Reference Clock
125MHz LVDS

ECS-LVDS33-1250-BN

125MHz LVDS



SFP Reference Clocks
125MHz CML

Engineer: M. Bogdan	The University of Chicago		
Drawn by: M. Bogdan	Digital Input and Clocks		
DATE: 9/15/2015	*Annie's Central Card*		
	REV. A	DRW. 2856	Sheet 2