

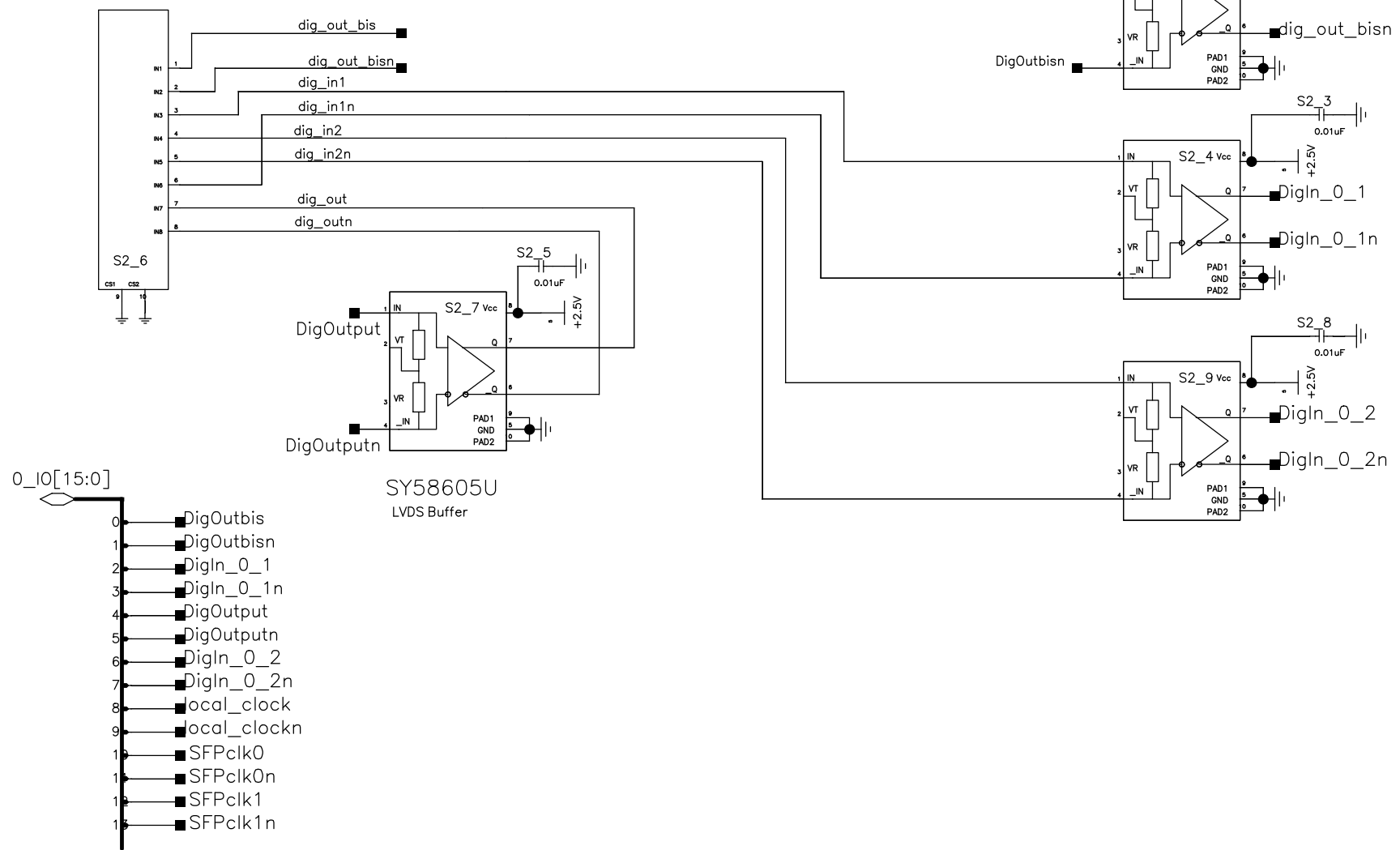
RJ45 Connector
AJT65B8813 or equivalent

A

B

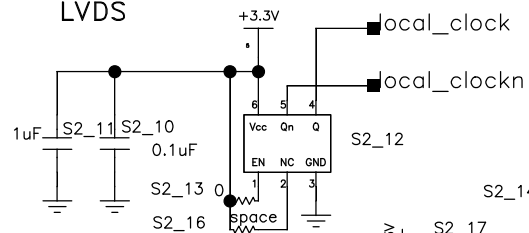
C

D

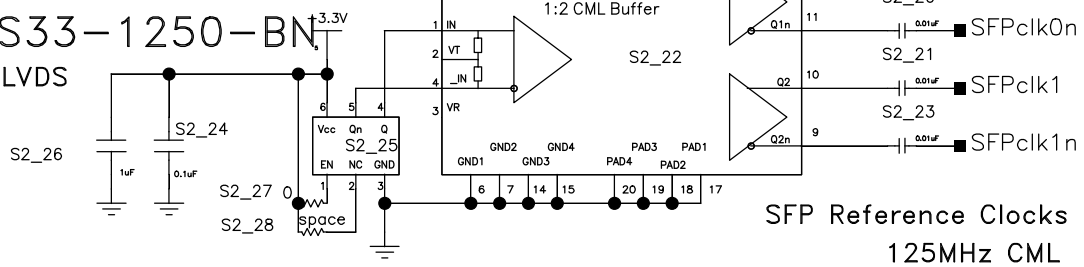


System Reference Clock
LVDS

Local Reference Clock
LVDS



ECS-LVDS33-1250-BN
125MHz LVDS



Engineer: M. Bogdan

Drawn by: M. Bogdan

DATE: 8/21/23

SPC#3019
ASM#3020

The University of Chicago
Acquisition and Control Module
DAMIC Digital Input and Clocks

REV. B | DRW. 3018 | Sheet 2